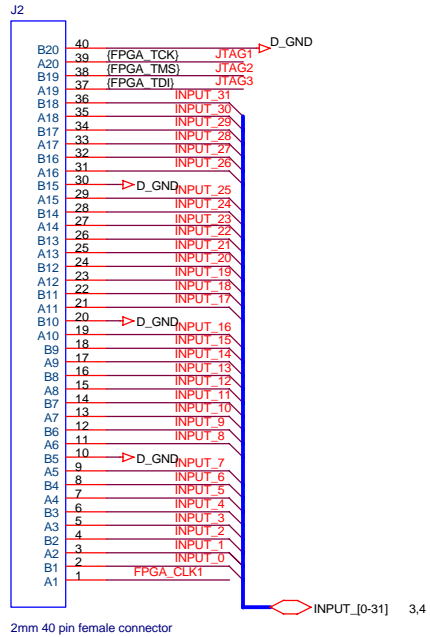


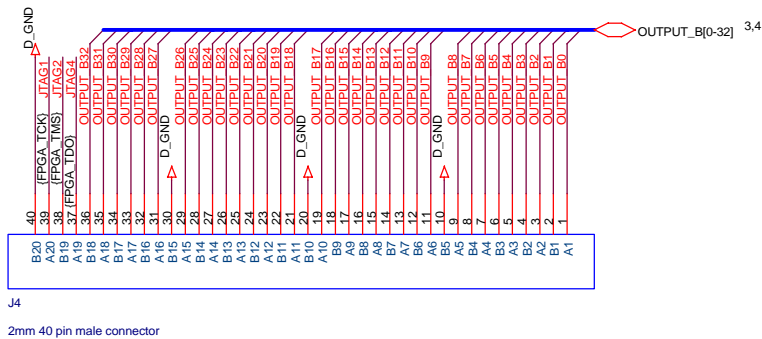
TABLE OF CONTENTS

MAIN	1	FPGA	4
+3.3V	2	MICRO	5
CONNECTOR	3	RS232	6

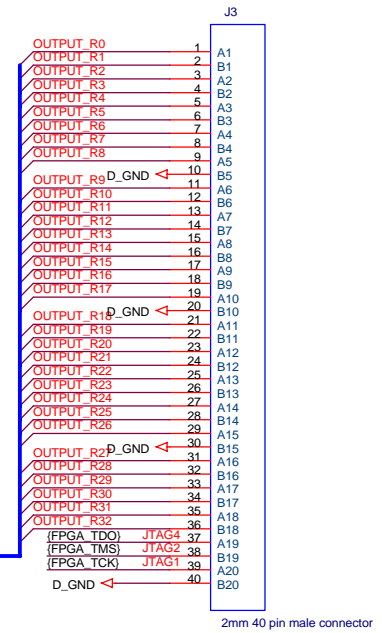
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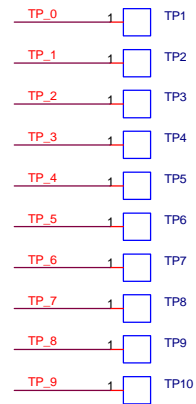
40 Pin OUTPUT - Bottom



40 Pin OUTPUT - Right



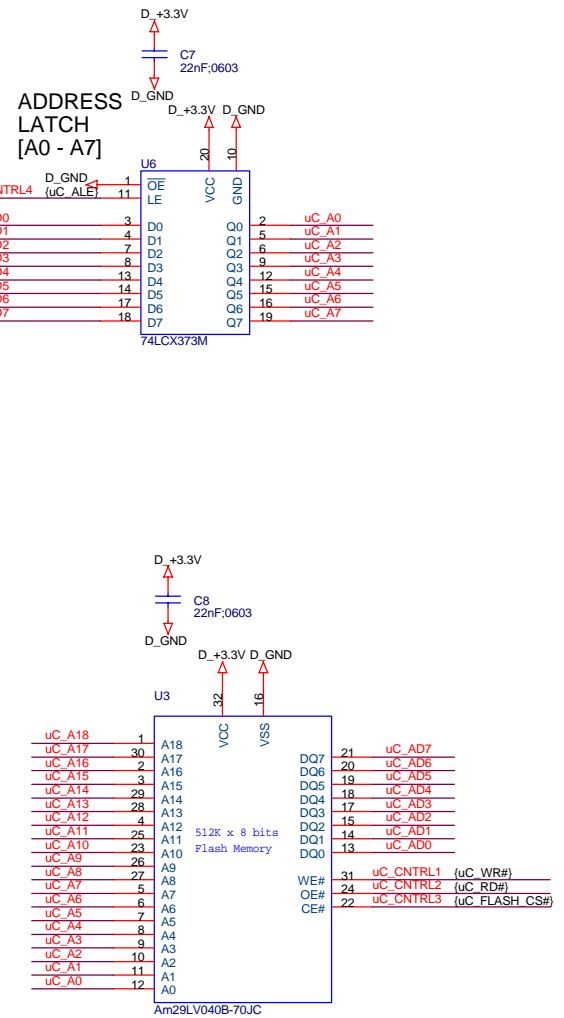
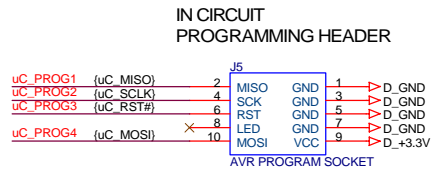
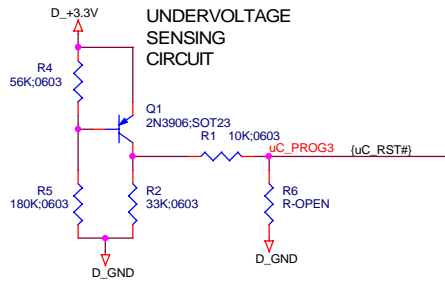
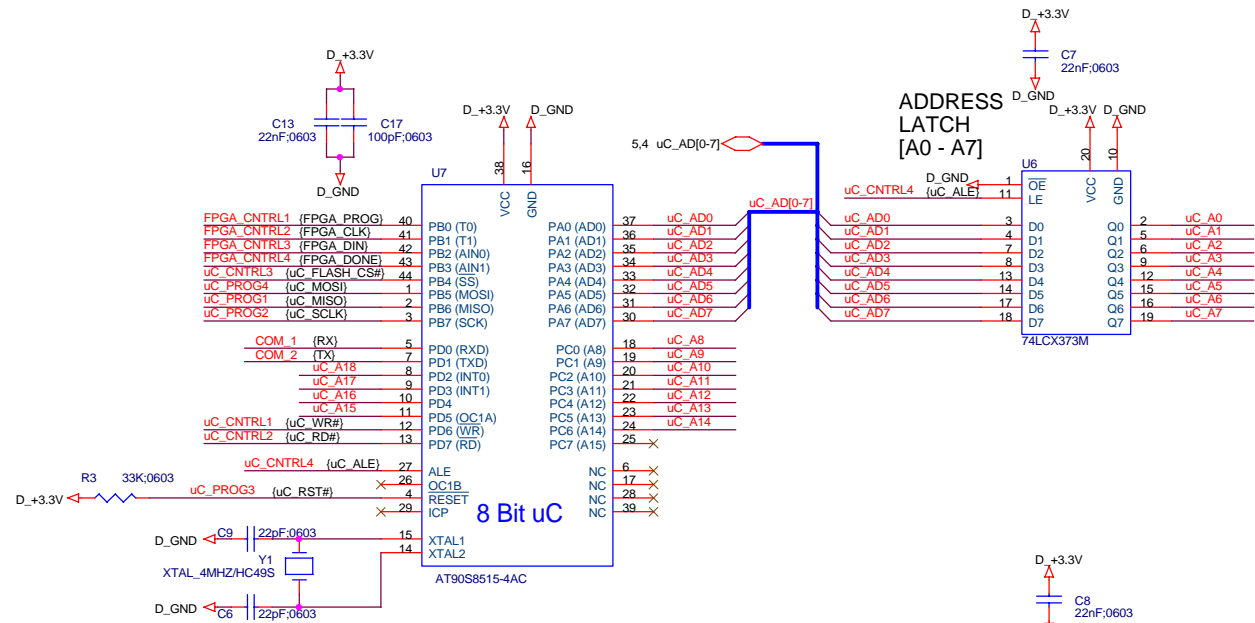
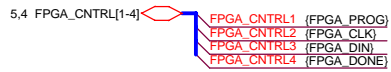
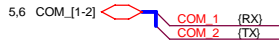
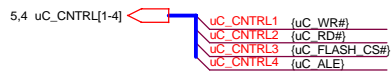
FPGA_CLK1



TEST POINTS



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Title ComBlock 200 / CONNECTOR		
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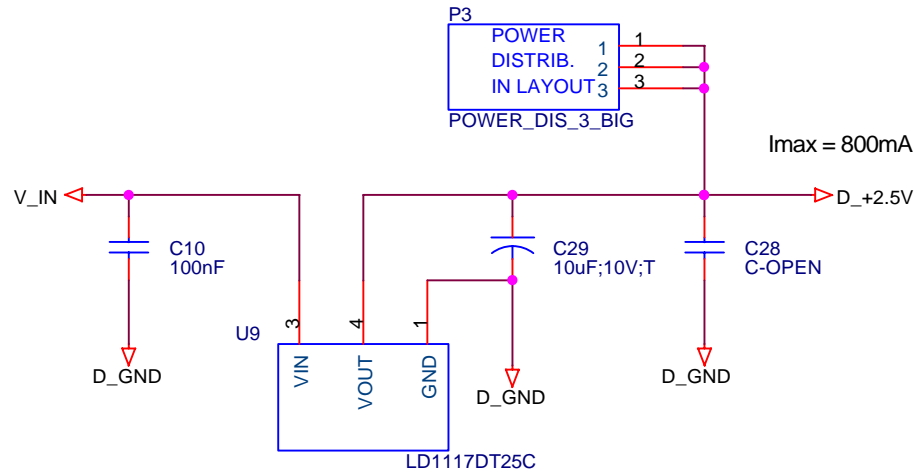
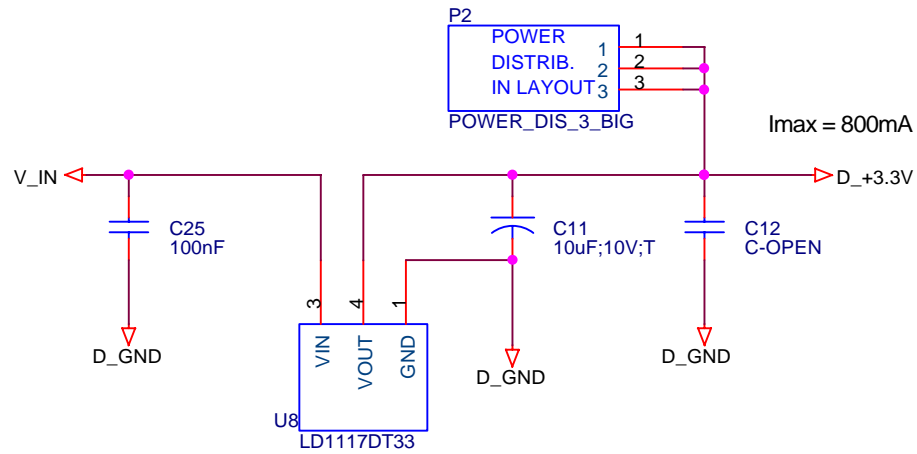


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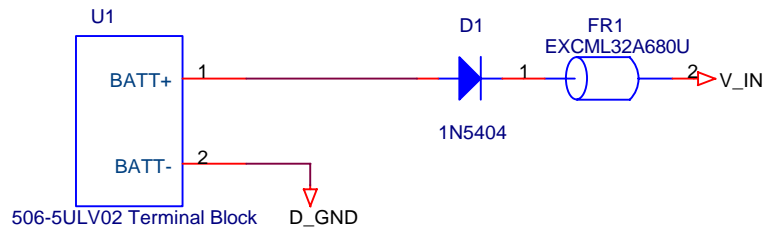
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Size B Document Number: **Y01006** Rev 0

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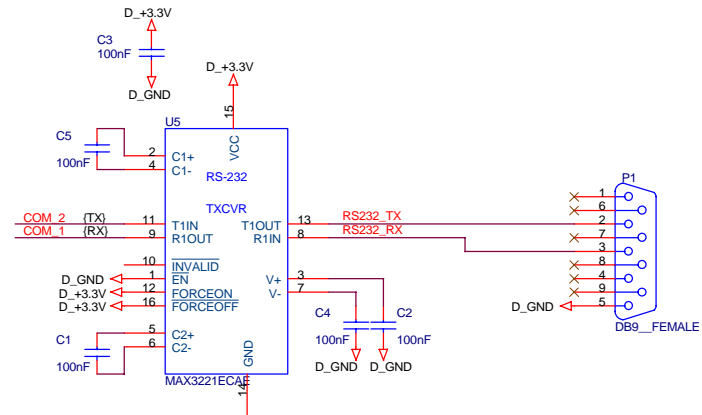


+5 V Supply

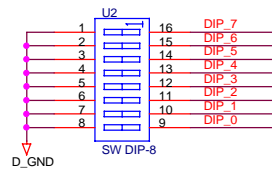
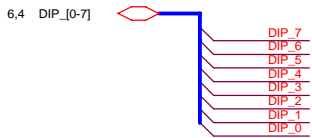


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DIP SWITCH



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