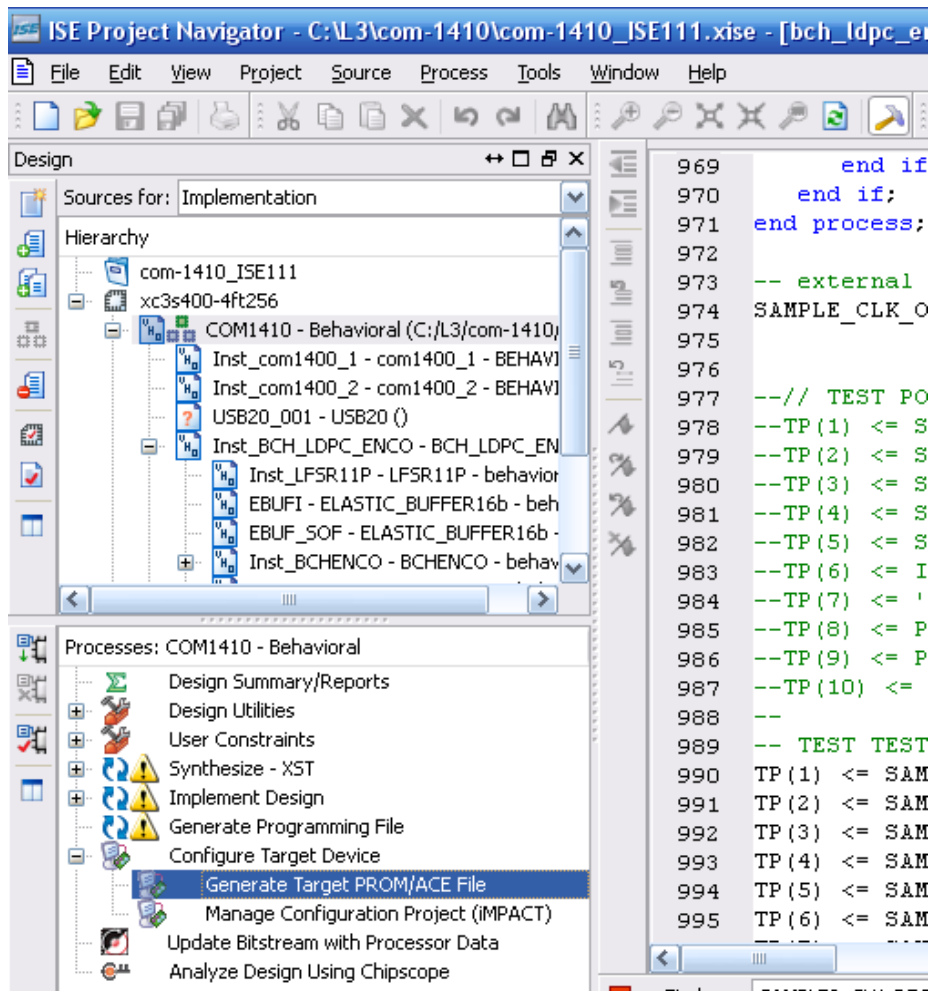


## CREATING A FPGA CONFIGURATION FILE (.MCS) USING XILINX ISE 11.1

All ComBlock FPGA development platforms store one or several FPGA configurations into Flash memory using .mcs formatted files as inputs. To convert a .bit FPGA configuration file produced by the Xilinx ISE project to a .mcs format, please use the following instructions.

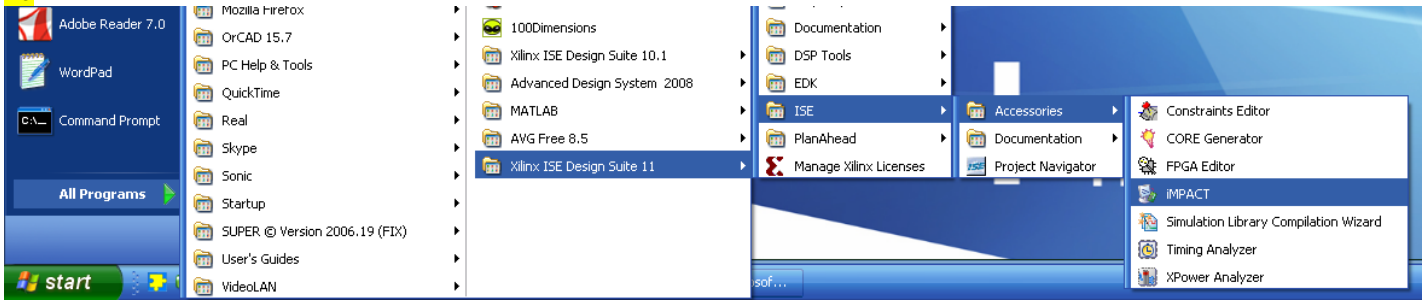
1a.



From the Processes window of the Xilinx ISE run “Generate Target PROM/ACE” under the Generate Programming File process.

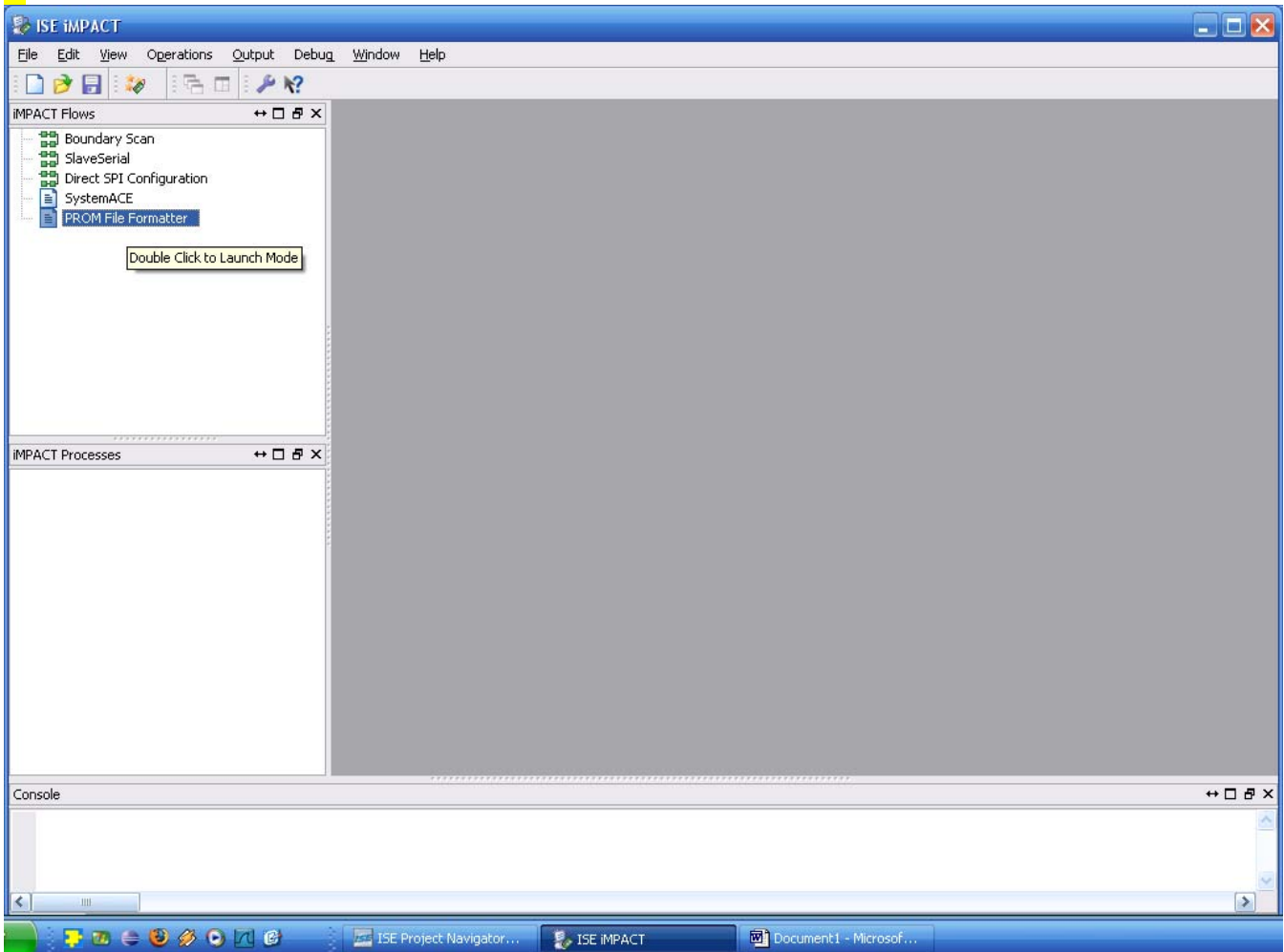
This will bring up iMPACT in a new window.

1b



As an alternative, iMPACT can be started from the start menu as illustrated above:

2.



Once the iMPACT window opens, select

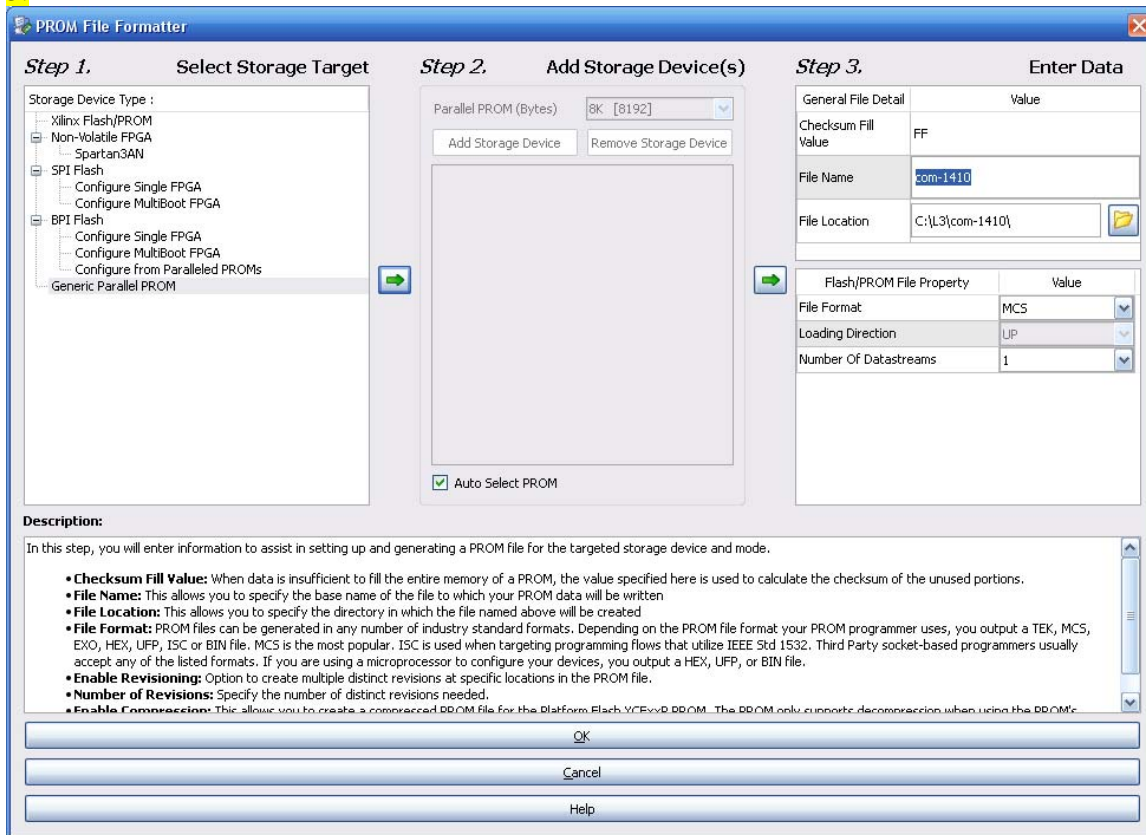
- PROM File Formatter



- Green arrow

- Autoselect PROM  Auto Select PROM

3.



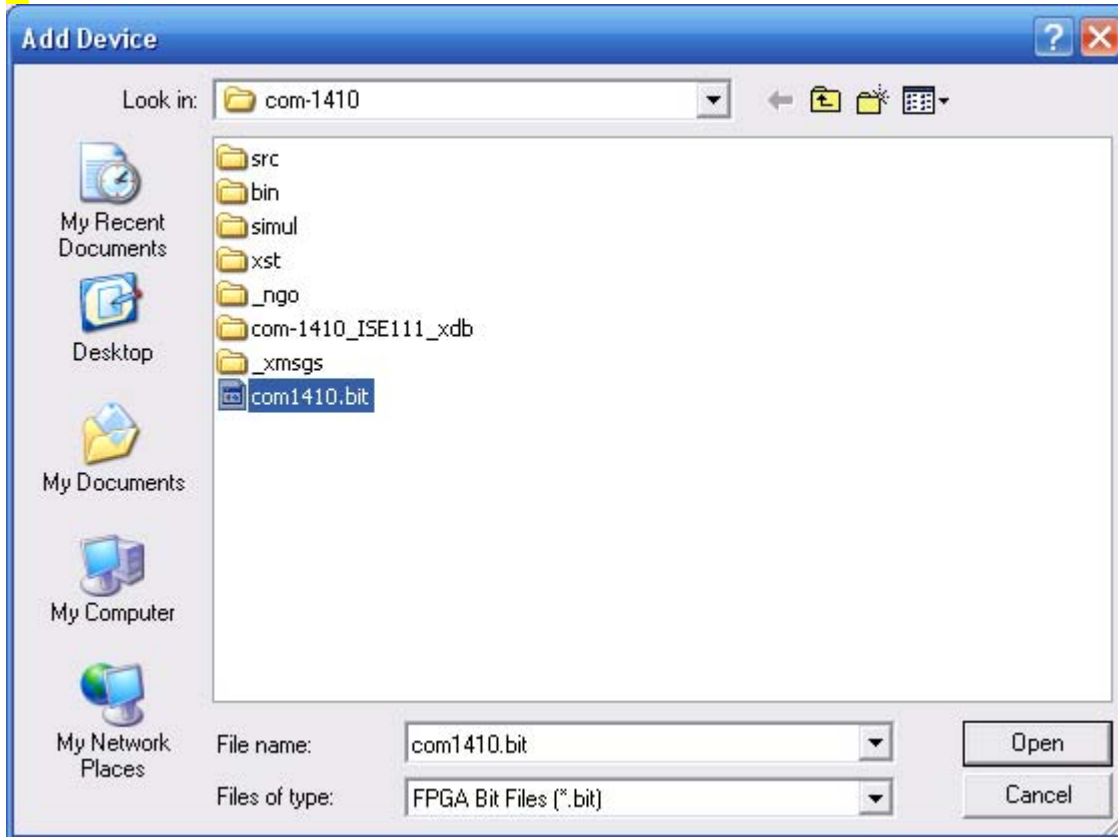
Enter the desired .mcs file name and click on the OK bar.

4.



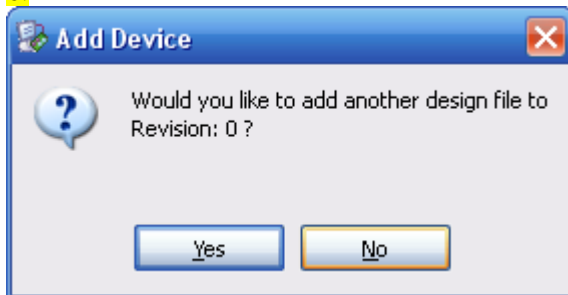
Click OK

5.

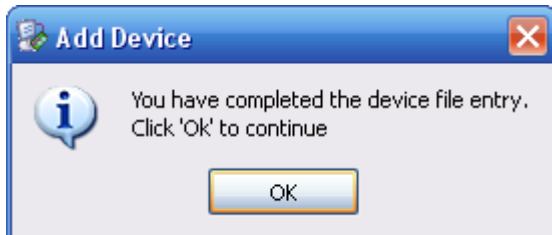


Point to the FPGA .bit file produced by the Xilinx ISE project. The FPGA .bit file contains the FPGA configuration to be loaded into Flash memory

6.

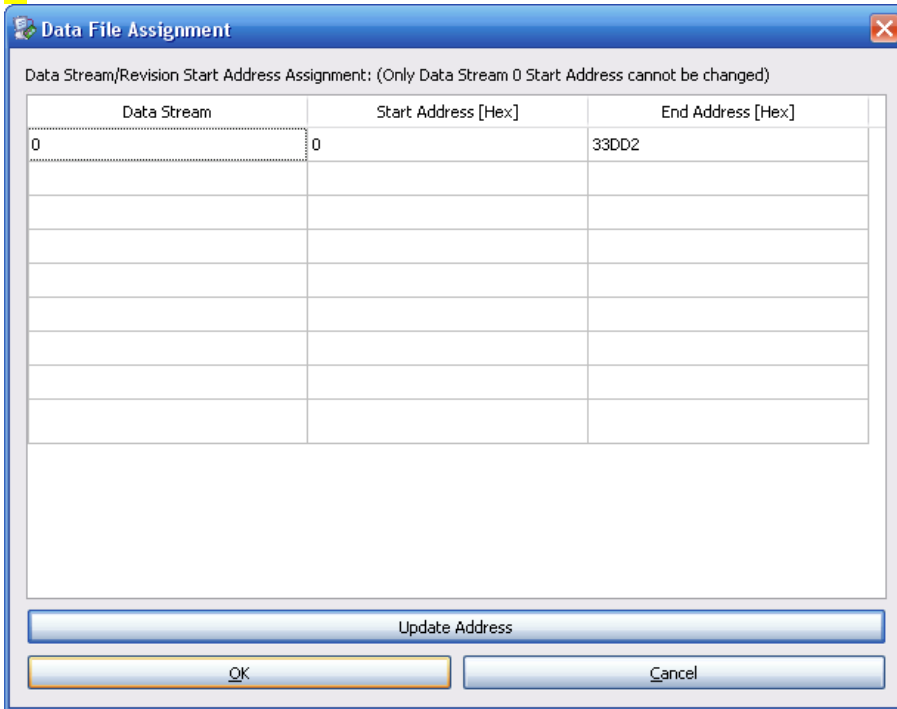


No



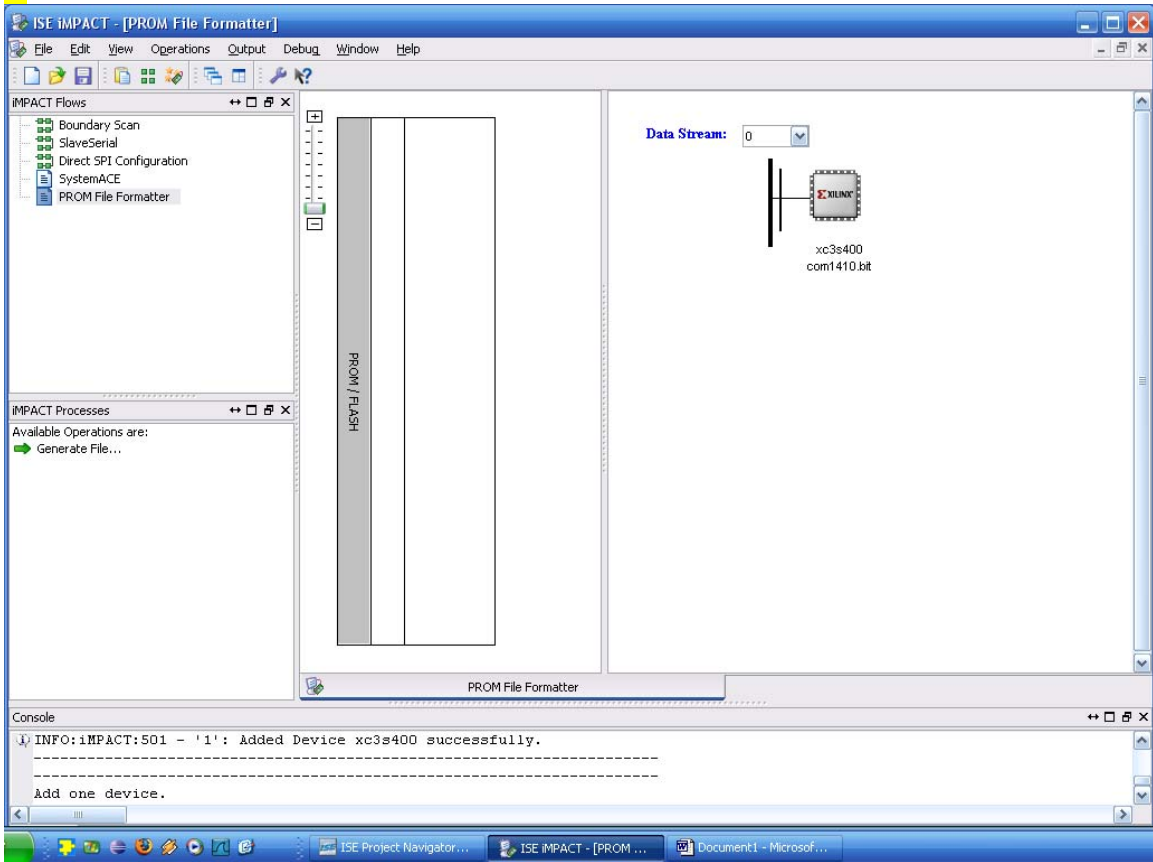
OK

7.



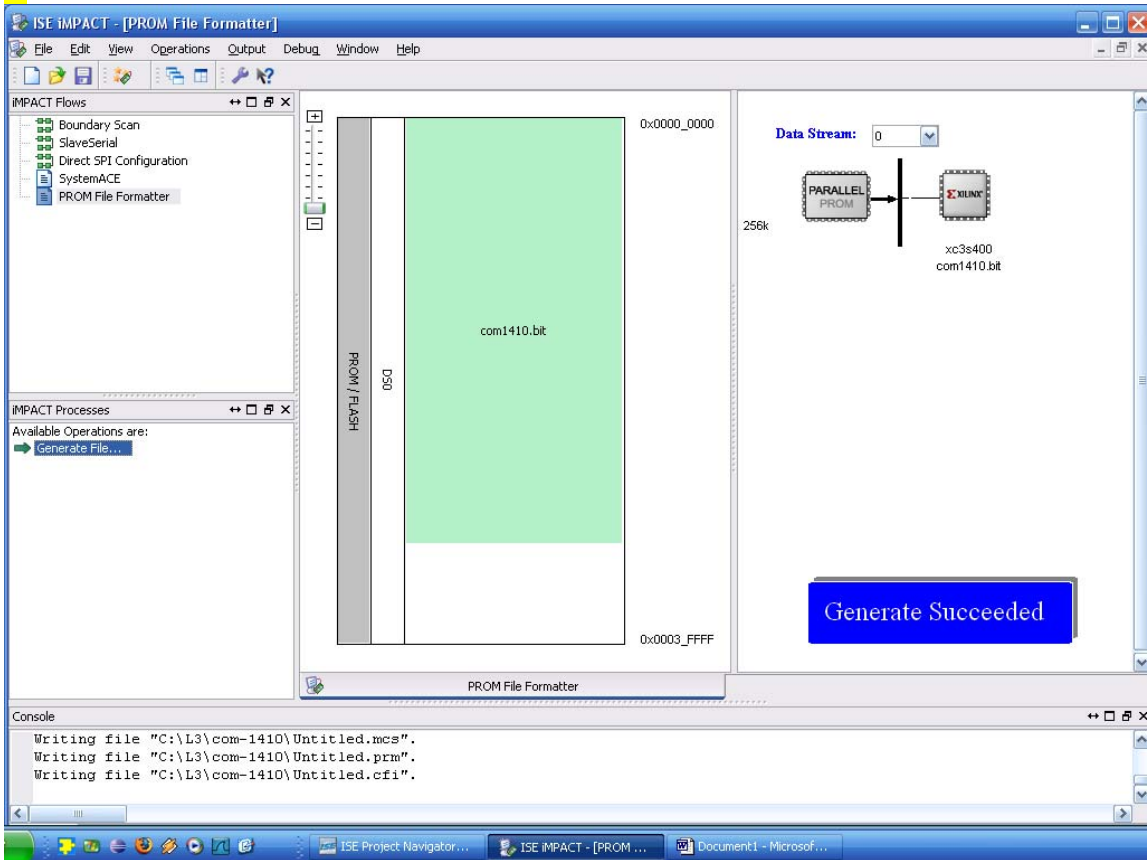
Click on OK to confirm that the .mcs file start address is indeed 0.

8.



Double click on the “Generate File” statement.

9.



In the final window, success is indicated by the blue banner “Generate Succeeded”  
Close iMPACT.

The resulting .mcs file can then be programmed into the ComBlock FPGA development platform using the supplied ComBlock Control Center software.