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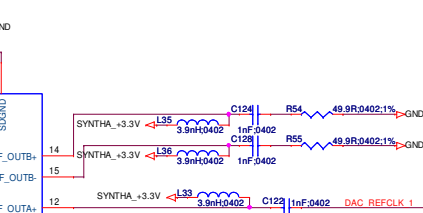
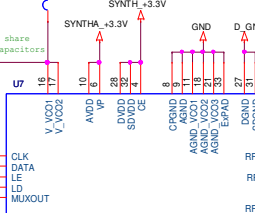
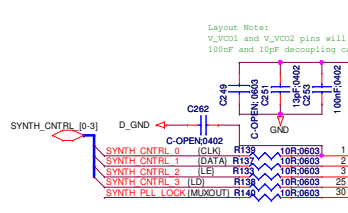
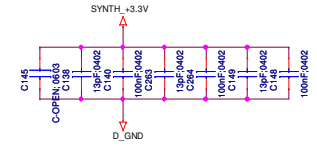
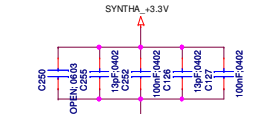
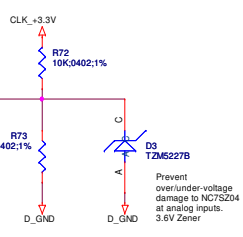
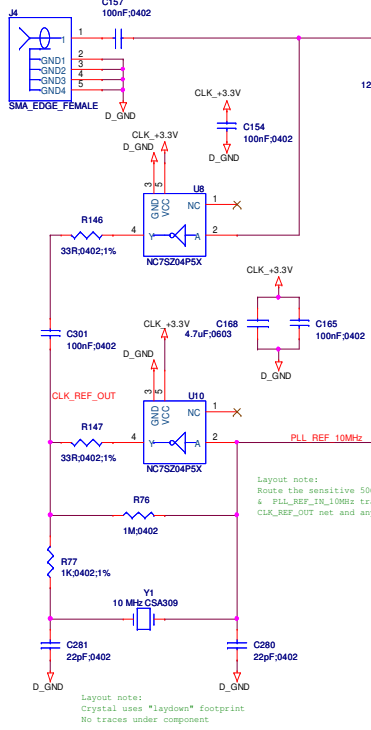
MAIN.....	1	DAC3.....	6
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DAC1.....	4	POWER.....	9
DAC2.....	5		

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Title		
MAIN		
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Layout Note:
Noise sources should be routed away from reference clock circuitry.

Layout Note:
SMA edge location should be the same with COM410 SMA location

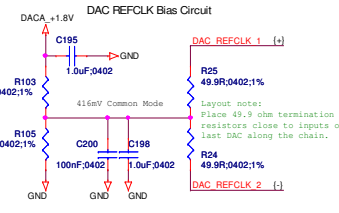
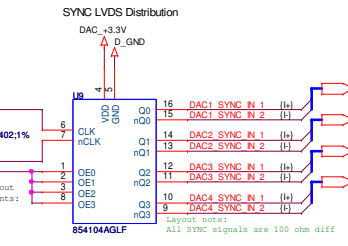
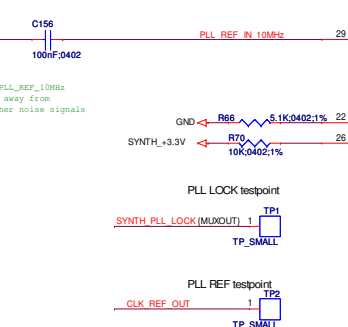
EXTERNAL FREQUENCY REFERENCE



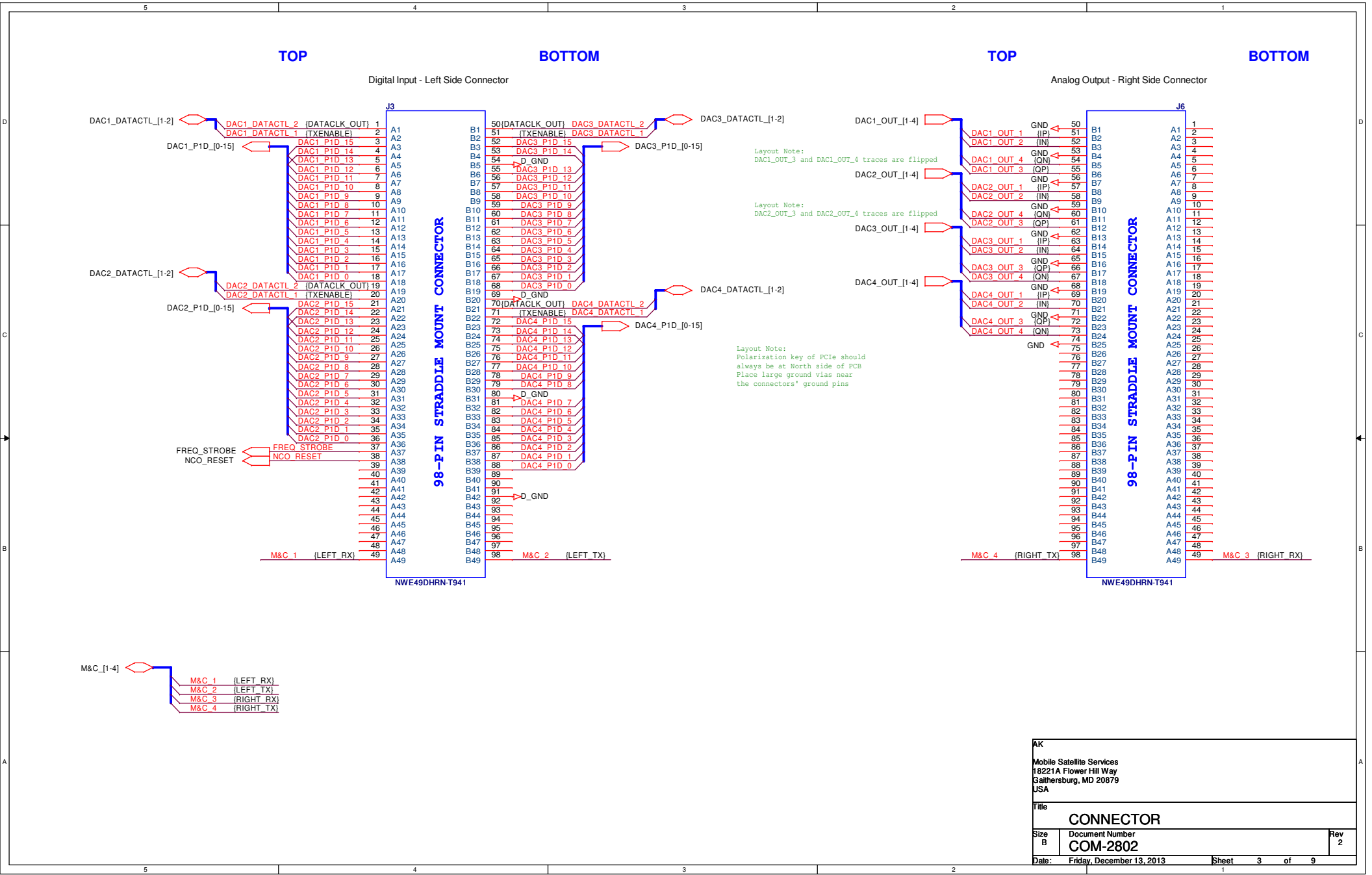
Layout note:
Daisy chain clock output from one DAC to the next instead of a fanout configuration

DAC REF OUTPUT is +5dBm(50 Ohm)

Layout note:
RF_OUTA+/- lines are 100 Ohm differential traces



Layout note:
Place 49.9 ohm termination resistors close to inputs of last DAC along the chain.

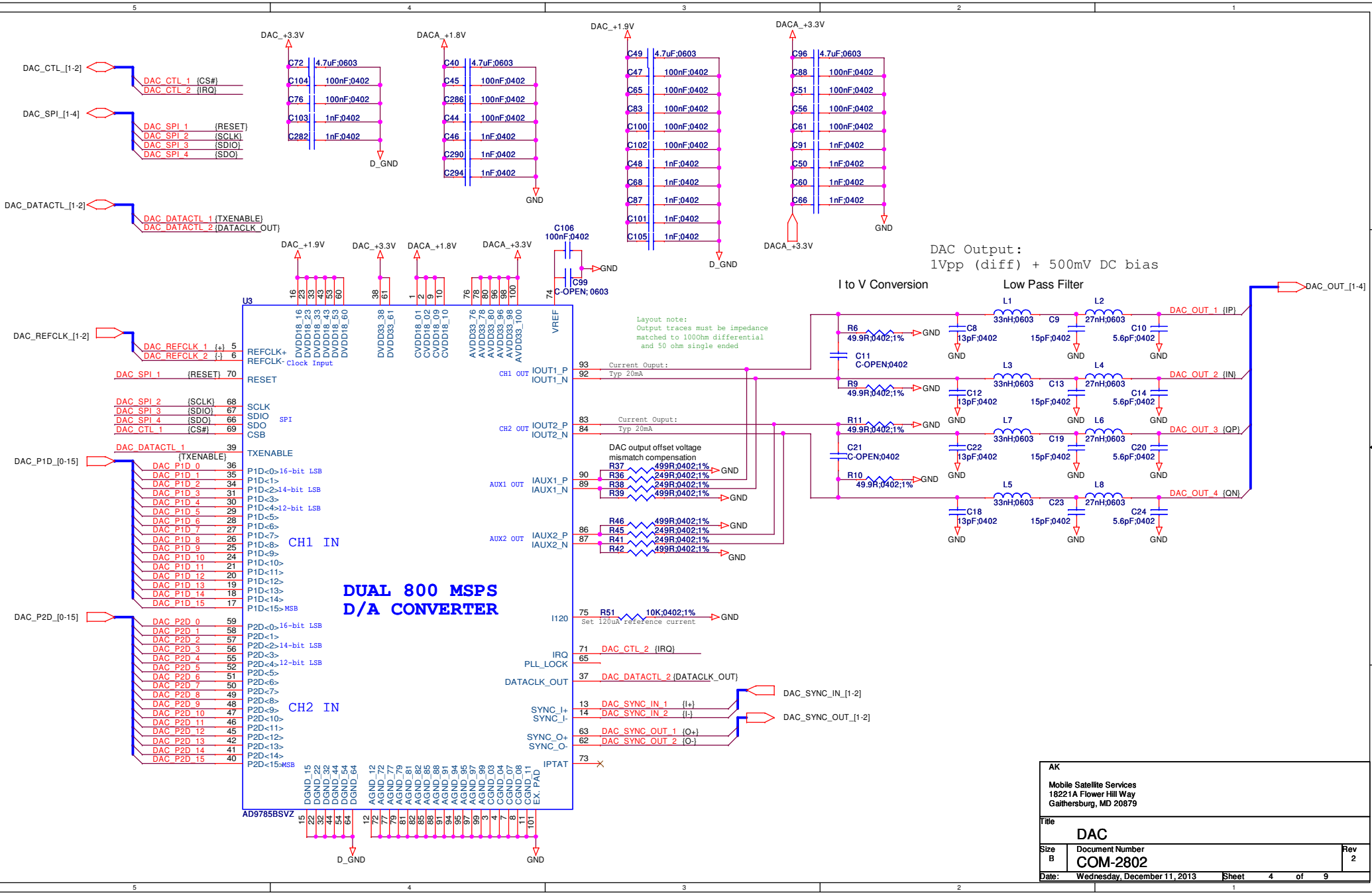


Layout Note:
DAC1_OUT_3 and DAC1_OUT_4 traces are flipped

Layout Note:
DAC2_OUT_3 and DAC2_OUT_4 traces are flipped

Layout Note:
Polarization key of PCIe should
always be at North side of PCB
Place large ground vias near
the connectors' ground pins

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Title CONNECTOR		
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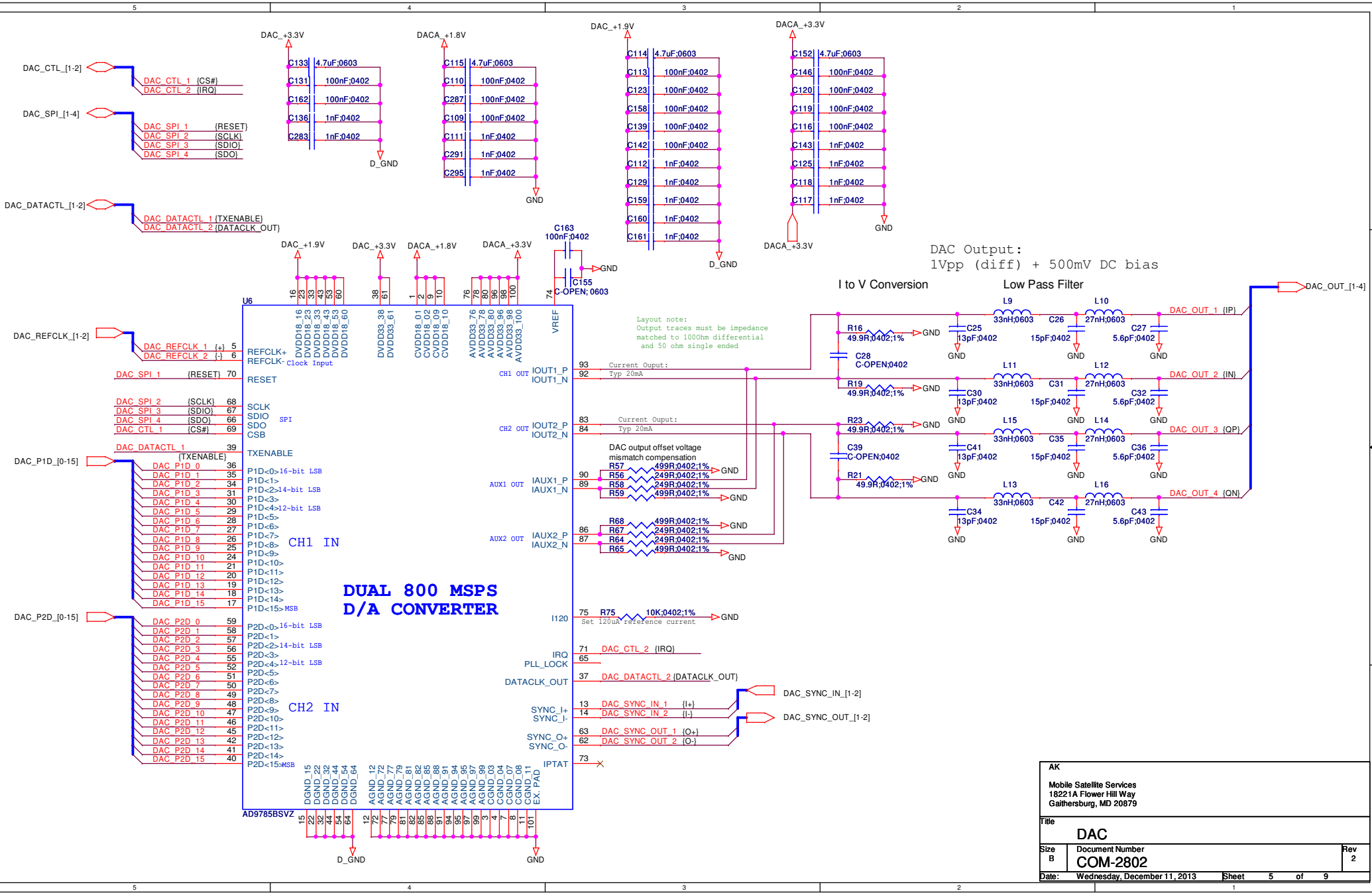
DAC Output:
1Vpp (diff) + 500mV DC bias

I to V Conversion Low Pass Filter

Layout note:
Output traces must be impedance
matched to 100ohm differential
and 50 ohm single ended

**DUAL 800 MSPS
D/A CONVERTER**

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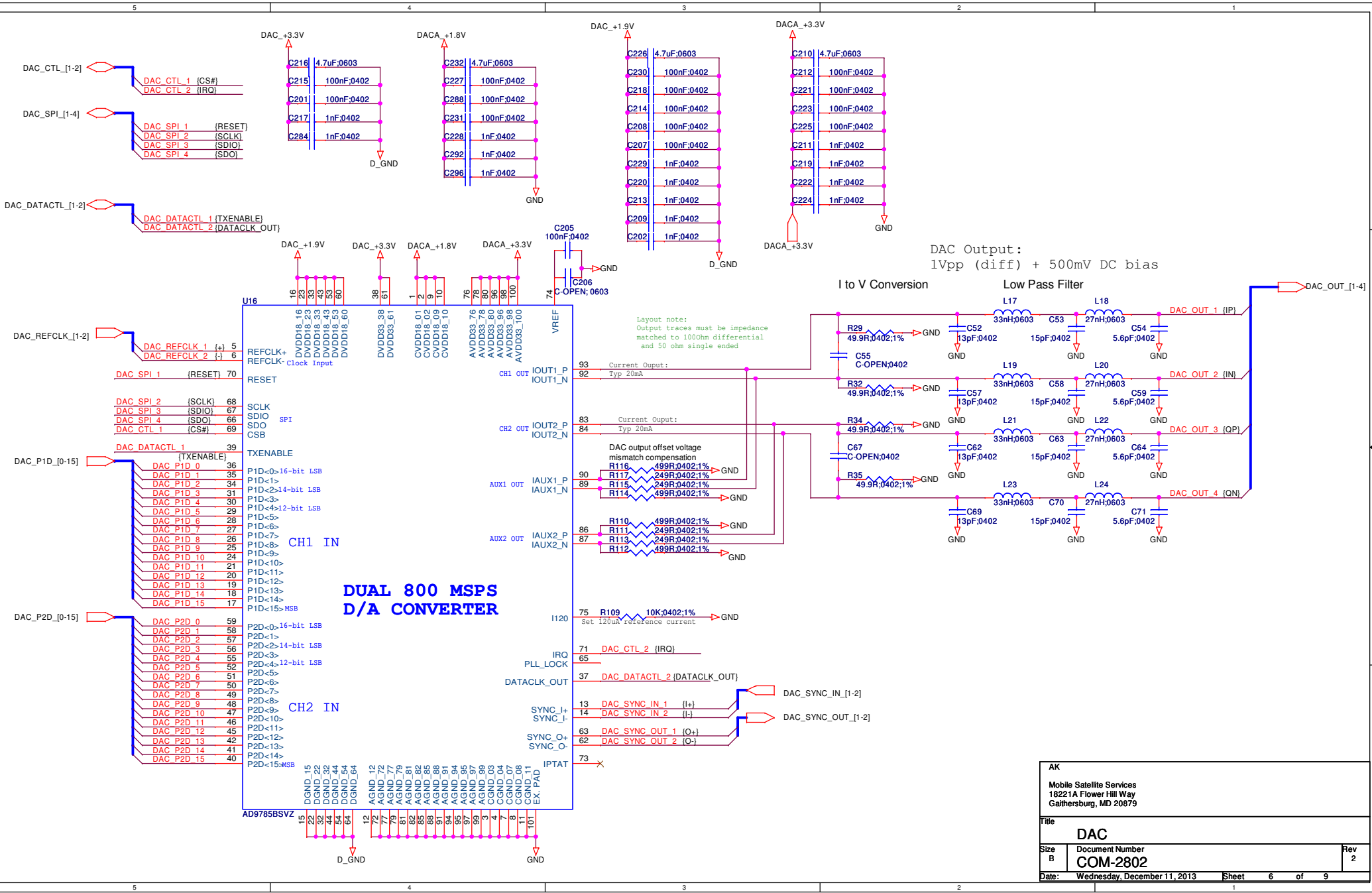
**DUAL 800 MSPS
D/A CONVERTER**

DAC Output:
1Vpp (diff) + 500mV DC bias

I to V Conversion Low Pass Filter

Layout note:
Output traces must be impedance
matched to 100ohm differential
and 50 ohm single ended

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DAC Output:
1Vpp (diff) + 500mV DC bias

I to V Conversion Low Pass Filter

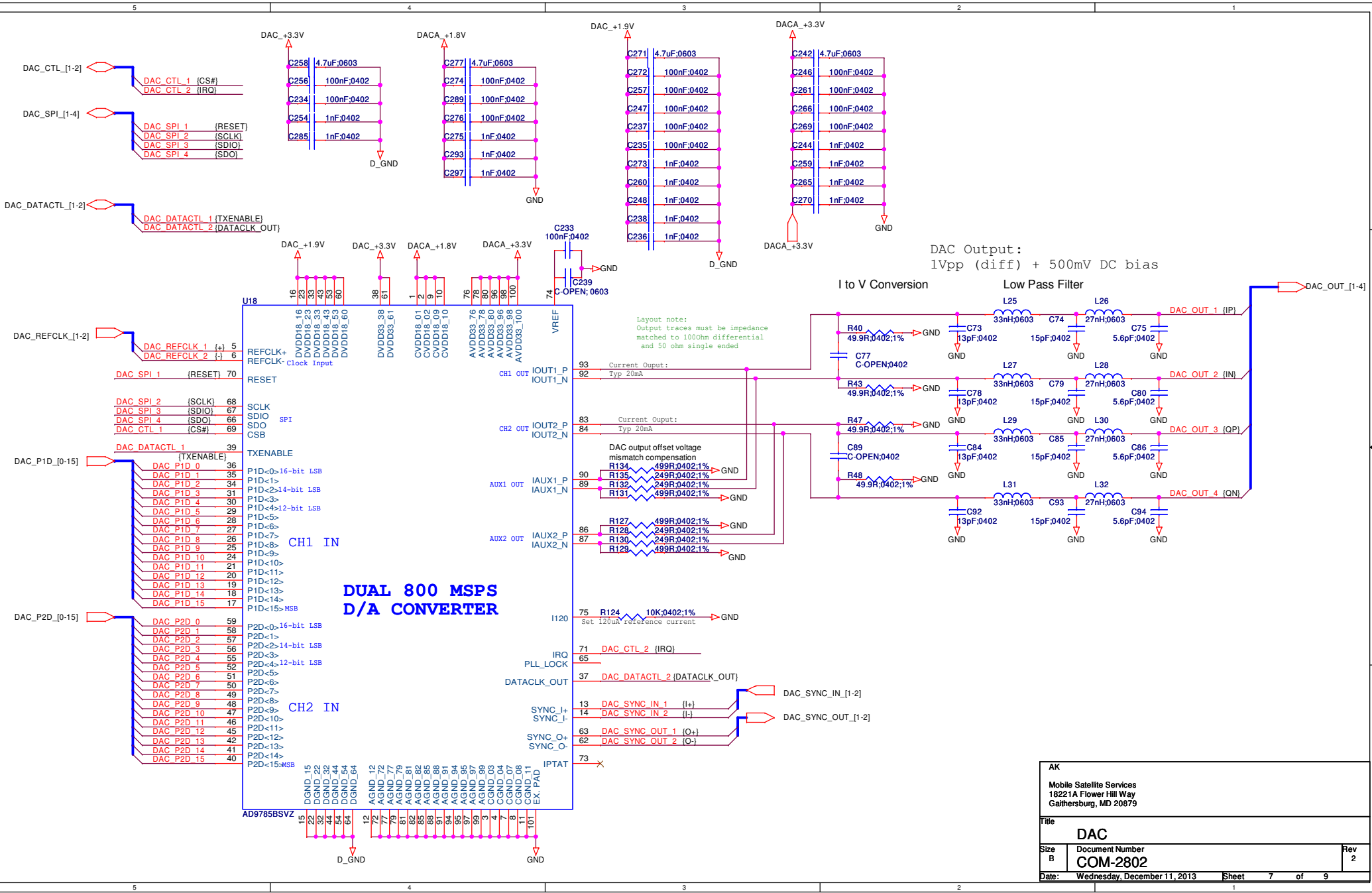
Layout note:
Output traces must be impedance
matched to 100ohm differential
and 50 ohm single ended

DAC output offset voltage
mismatch compensation
R116 499R.0402:1% → GND
R117 249R.0402:1% → GND
R115 249R.0402:1% → GND
R114 499R.0402:1% → GND
R110 499R.0402:1% → GND
R111 249R.0402:1% → GND
R113 249R.0402:1% → GND
R112 499R.0402:1% → GND

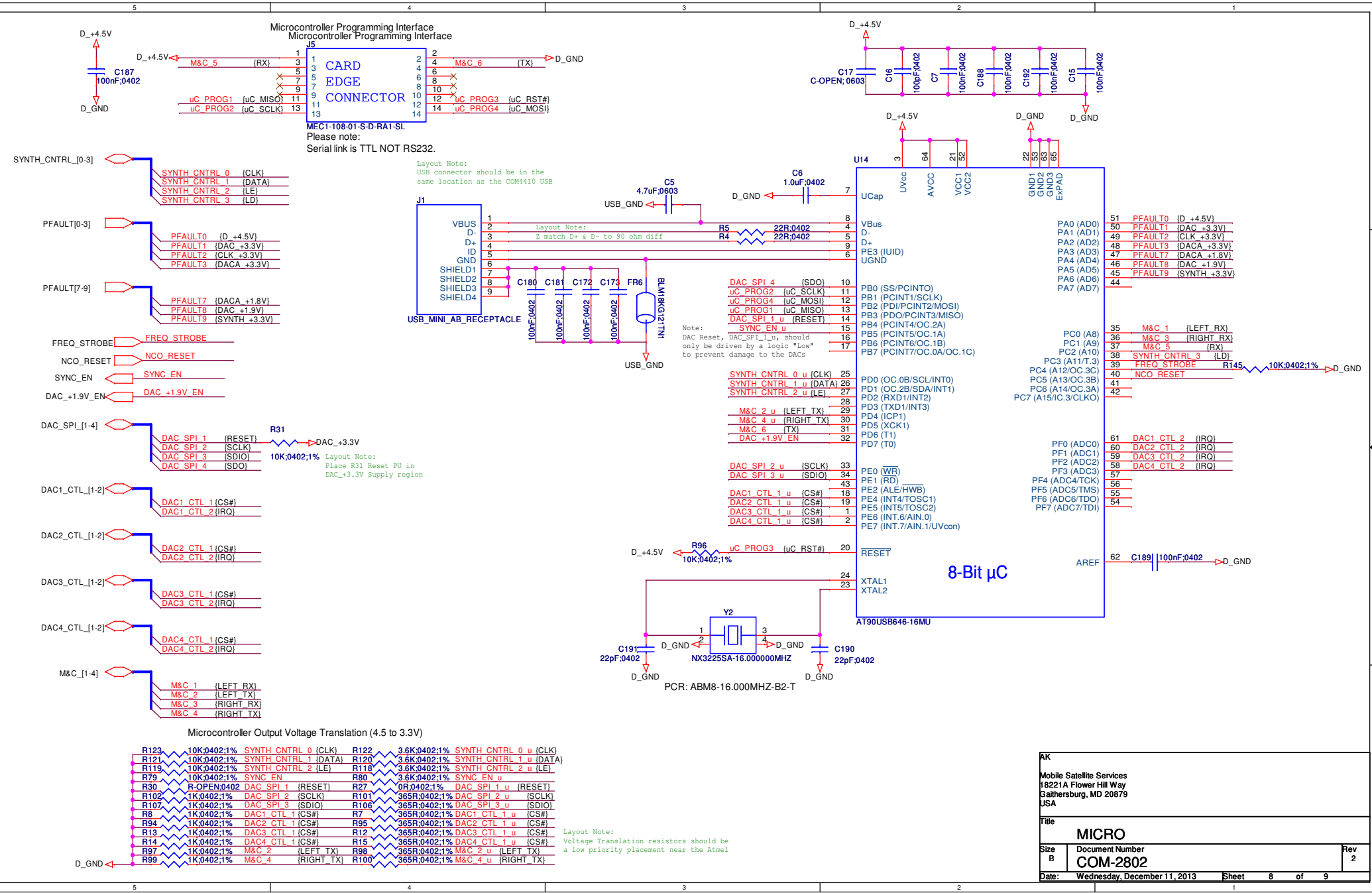
R109 10K.0402:1% → GND
Set 120uA reference current

DUAL 800 MSPS D/A CONVERTER

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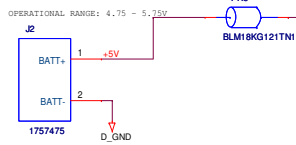


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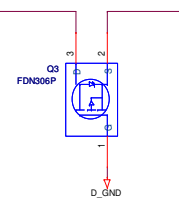


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+5V Supply

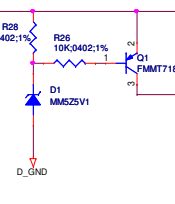


Reverse Voltage Protection



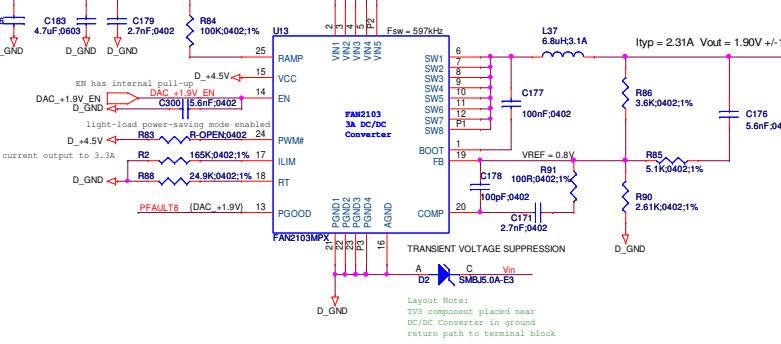
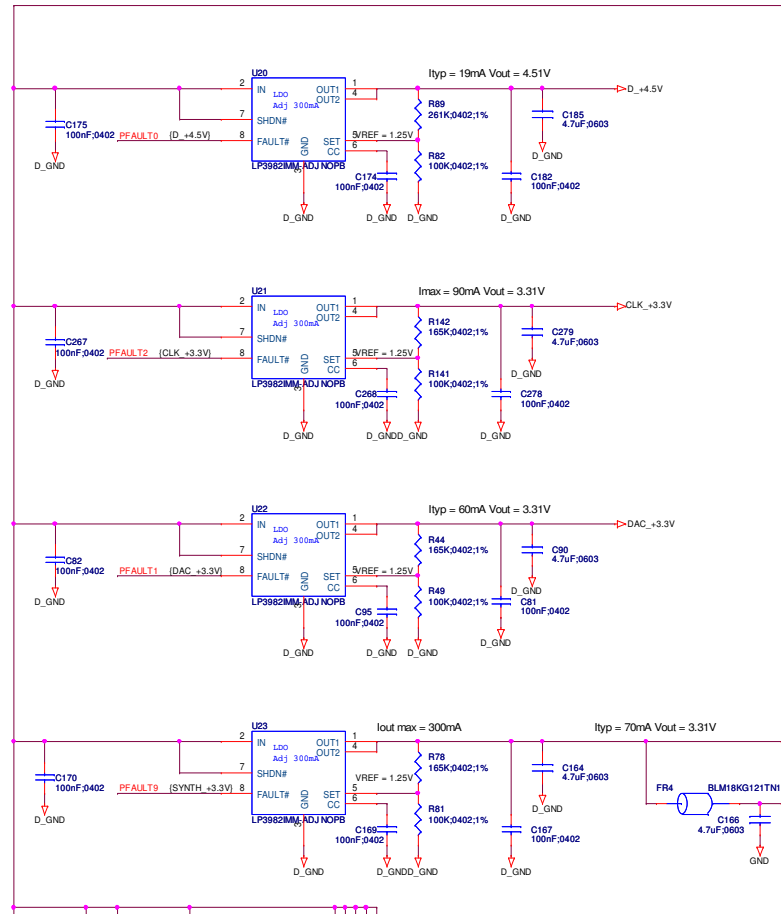
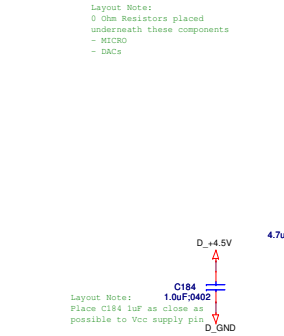
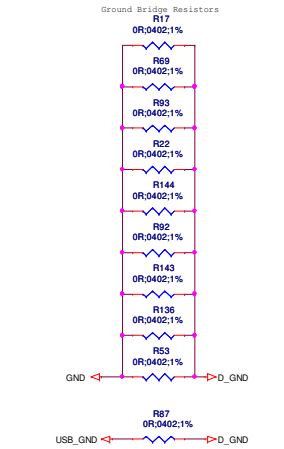
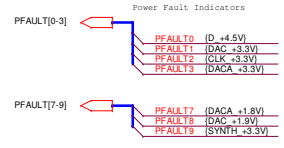
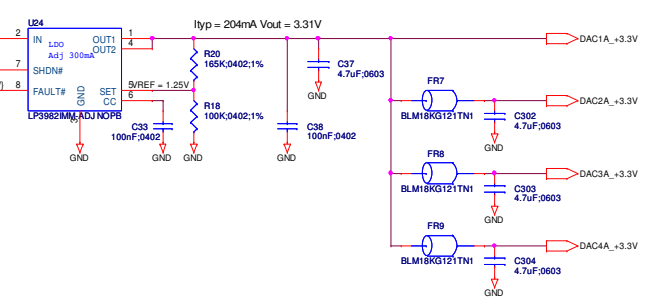
Digital Supplies

Over Voltage Protection (5.85V)



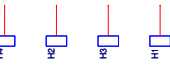
Note: If C2 voltage dropout is too great use Zetex ZLTP1902DFFTA

Analog DAC Supplies



COMP	DIGITAL V	mA	ANALOG V	mA
ATMEL	+4.5	<30		
CLK	+3.3	<90		
DAC1	+3.3	15	+3.3	51
DAC2	+3.3	15	+3.3	51
DAC3	+3.3	15	+3.3	51
DAC4	+3.3	15	+3.3	51
DAC1	+1.9	580	+1.8	50
DAC2	+1.9	580	+1.8	50
DAC3	+1.9	580	+1.8	50
DAC4	+1.9	580	+1.8	50

PCB Mounting Holes



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POWER

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