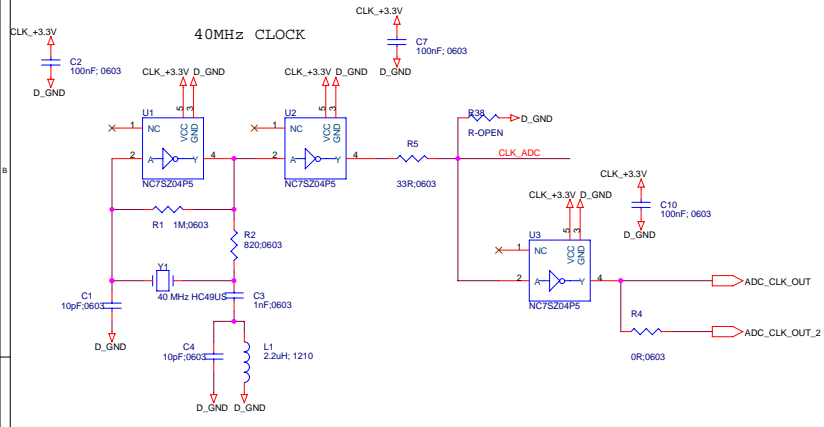
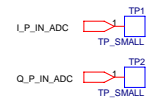
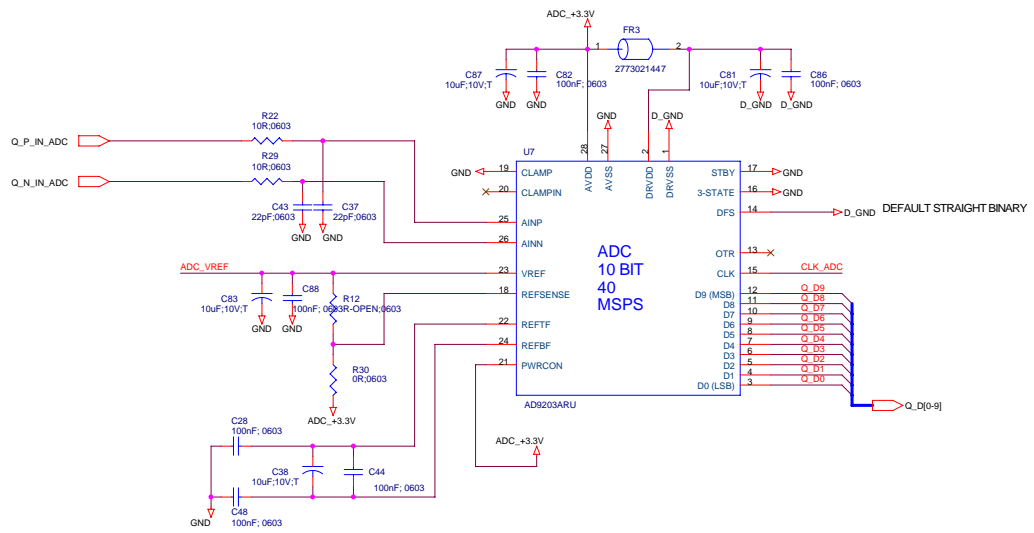
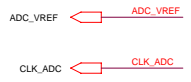
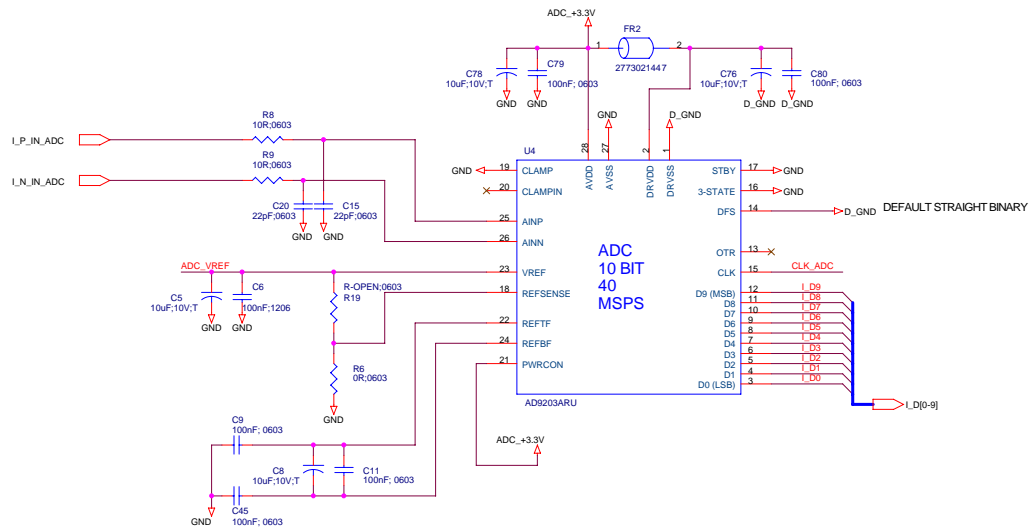
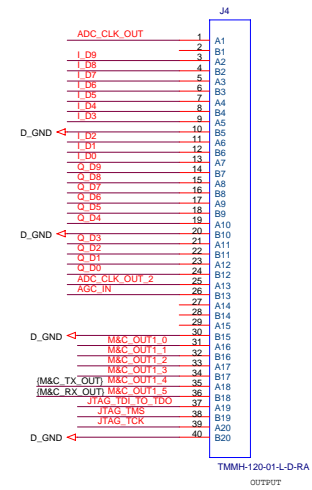
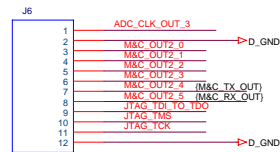
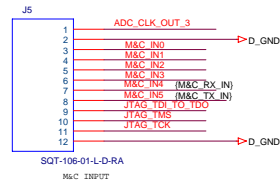
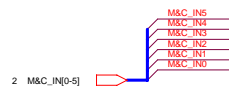
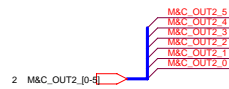
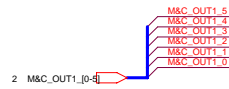
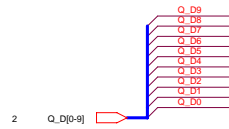
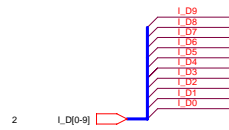


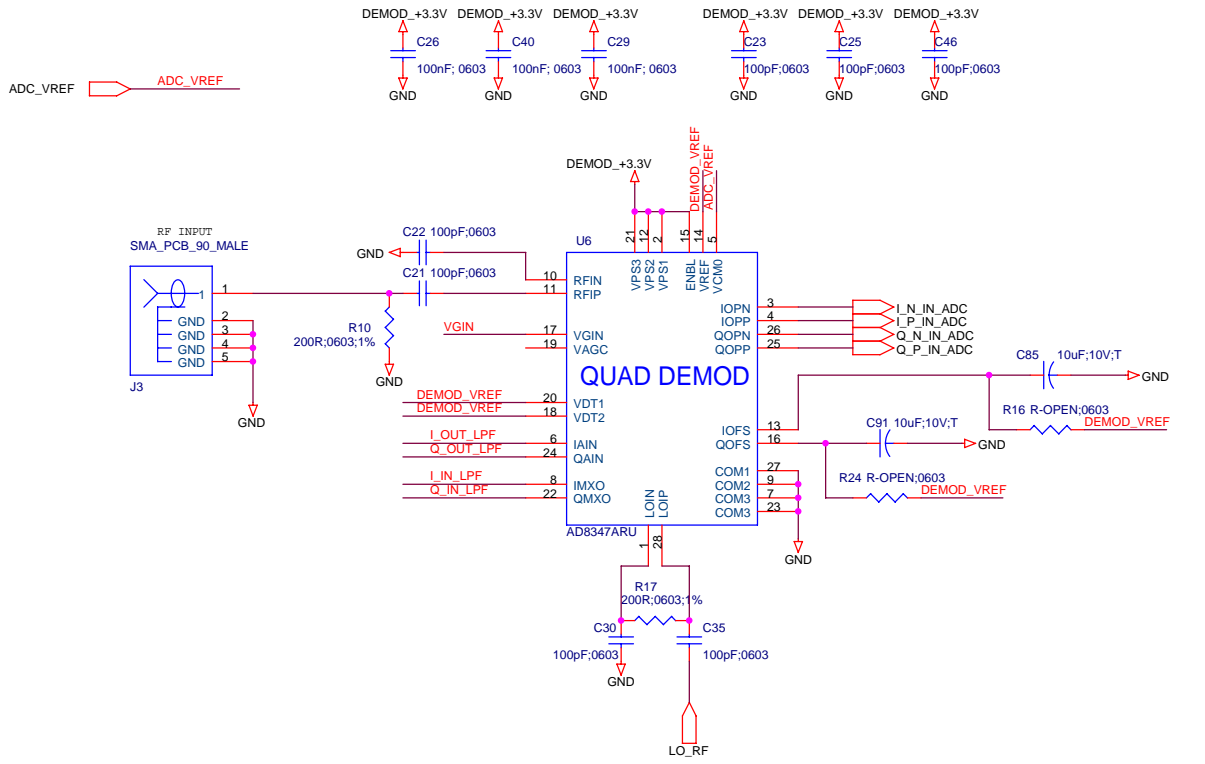
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 FREQ SYNTH..... 5
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 POWER..... 7
 LAYOUT..... 8

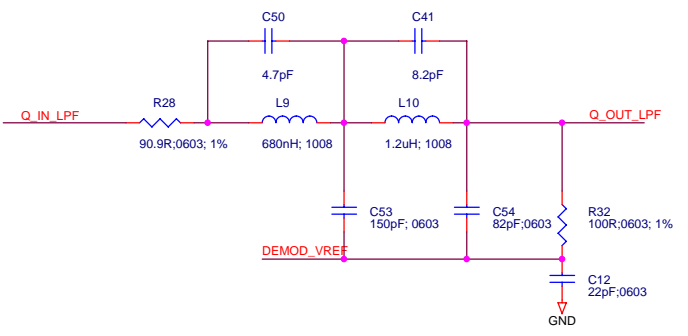
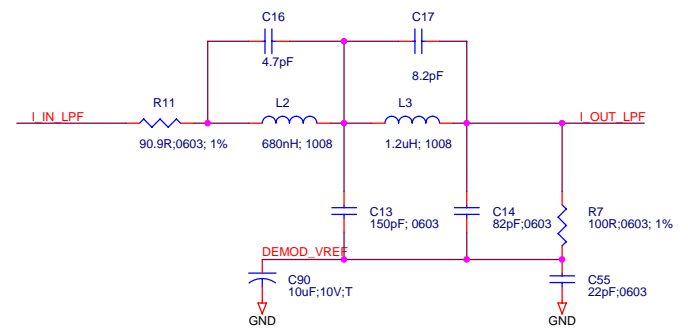


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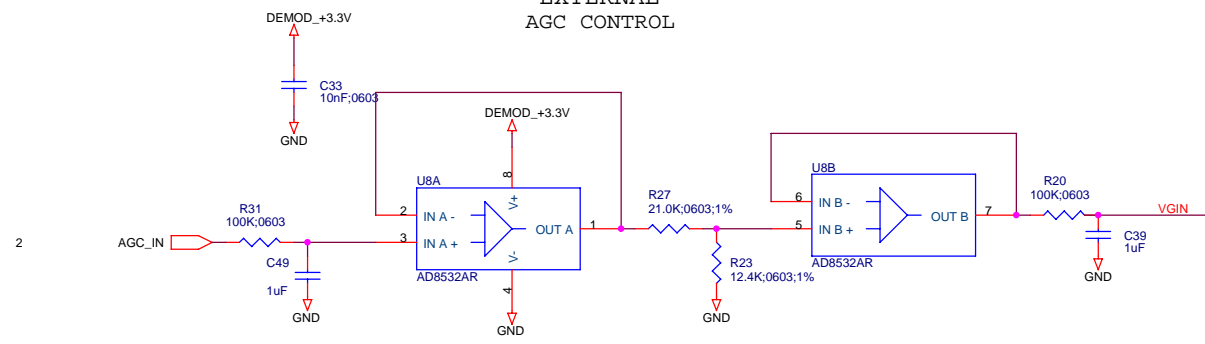




Elliptic Low Pass Filter
 100 Ohm
 4th Order
 F(cut-off) = 20MHz

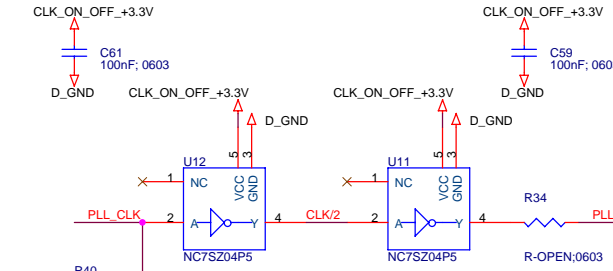
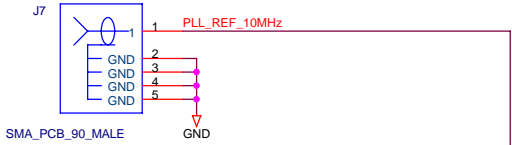


EXTERNAL AGC CONTROL

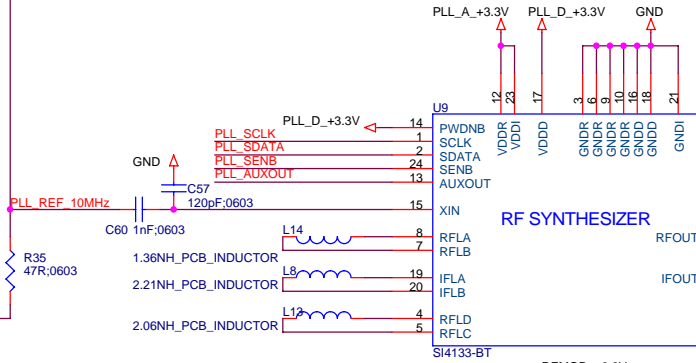
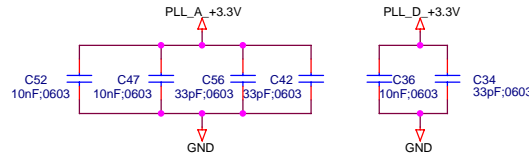
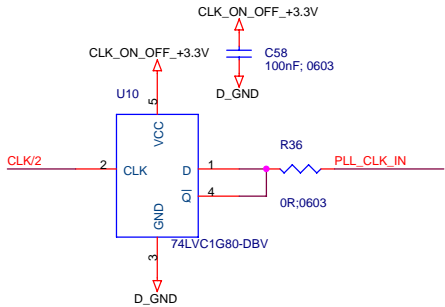


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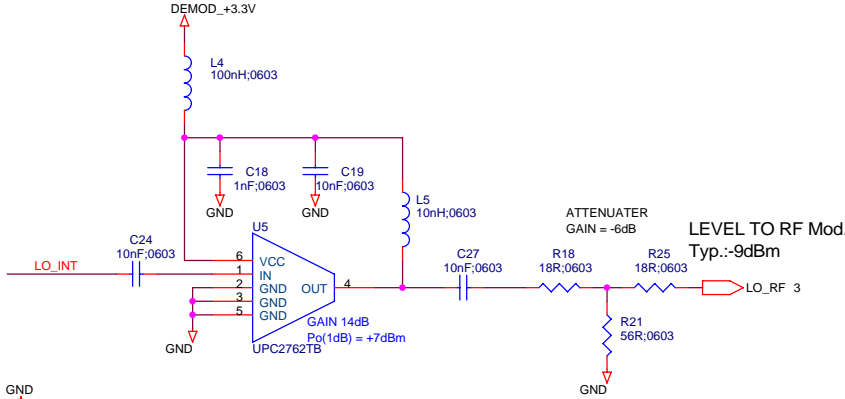
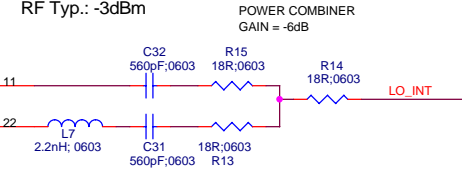
EXTERNAL FREQUENCY REFERENCE



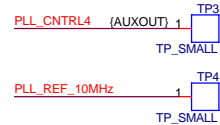
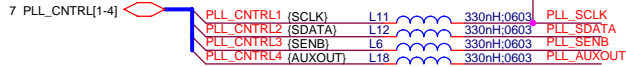
CLOCK DIVIDE BY 2 40MHz IN -- 20MHz OUT



LEVEL FROM MIXER:
RF Typ.: -3dBm



LEVEL TO RF Mod.:
Typ.: -9dBm



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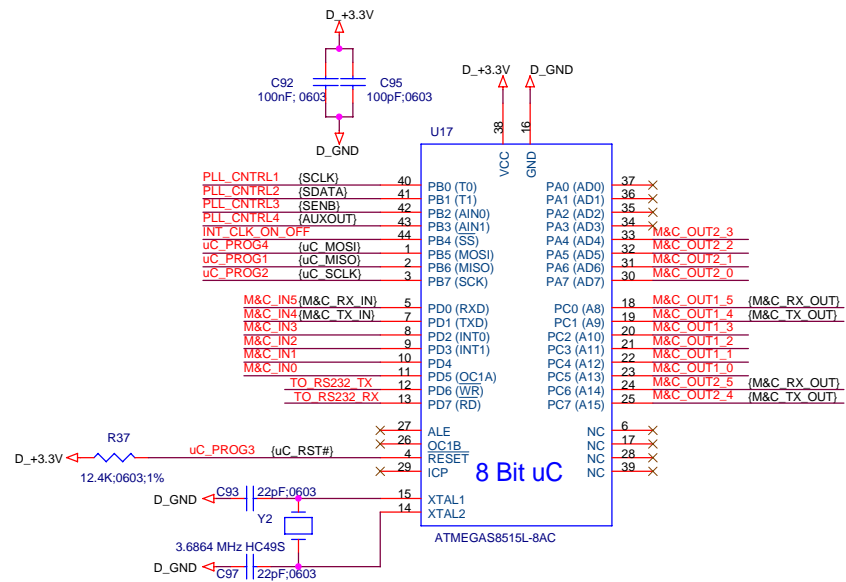
4 INT_CLK_ON_OFF ← INT_CLK_ON_OFF

2 M&C_OUT1_[0-4] ← M&C_OUT1_5
M&C_OUT1_4
M&C_OUT1_3
M&C_OUT1_2
M&C_OUT1_1
M&C_OUT1_0

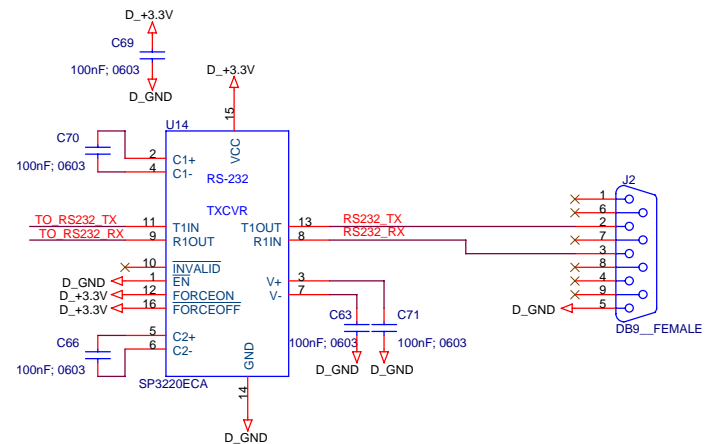
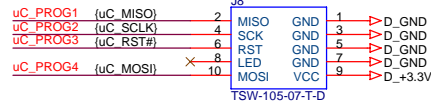
2 M&C_OUT2_[0-4] ← M&C_OUT2_5
M&C_OUT2_4
M&C_OUT2_3
M&C_OUT2_2
M&C_OUT2_1
M&C_OUT2_0

2 M&C_IN_[0-5] ← M&C_IN5
M&C_IN4
M&C_IN3
M&C_IN2
M&C_IN1
M&C_IN0

2 PLL_CNTRL_[1-4] ← PLL_CNTRL1 (SCLK)
PLL_CNTRL2 (SDATA)
PLL_CNTRL3 (SENB)
PLL_CNTRL4 (AUXOUT)



IN CIRCUIT PROGRAMMING HEADER



Mobile Satellite Services

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Gaithersburg, MD 20879
USA

Title
ComBlock L Band RX / MICRO

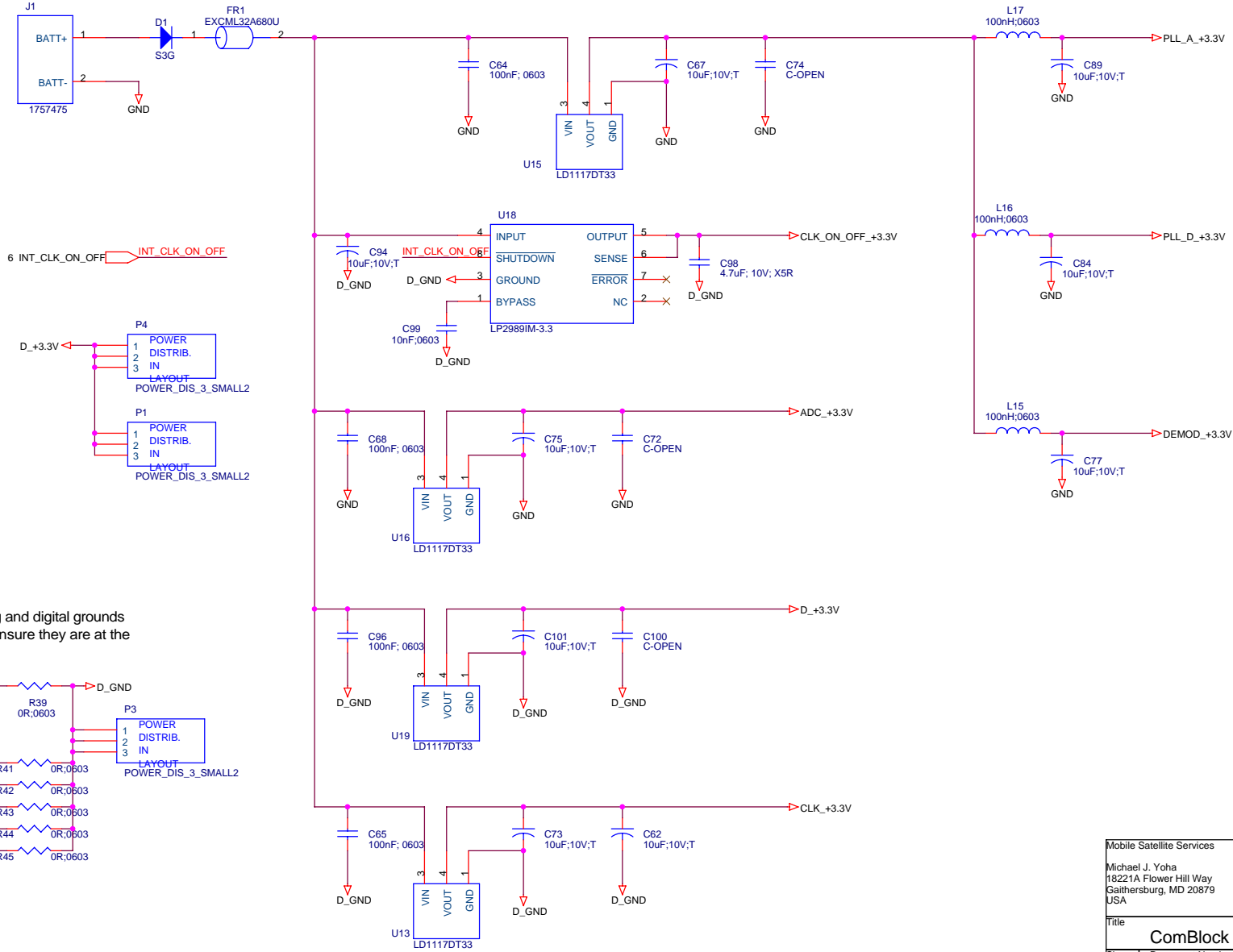
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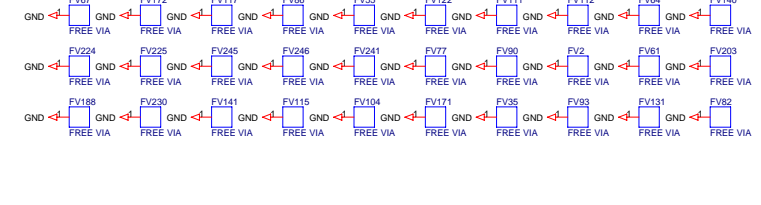
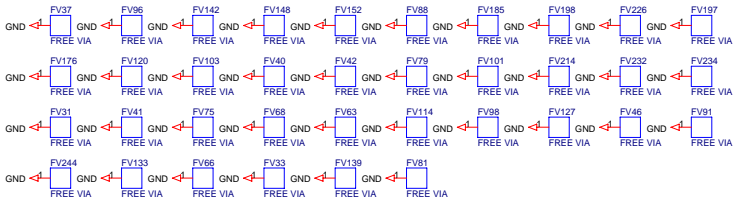
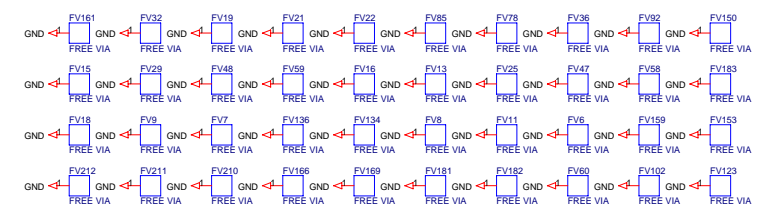
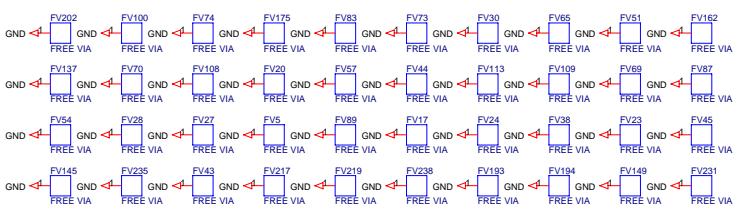
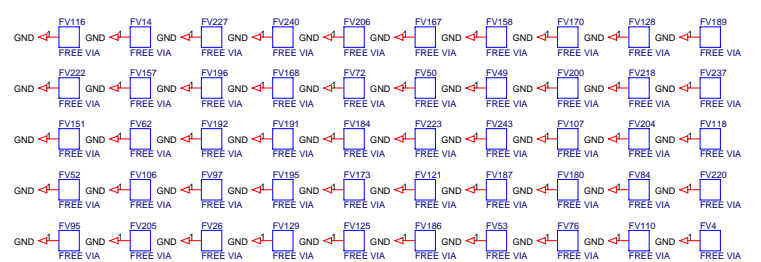
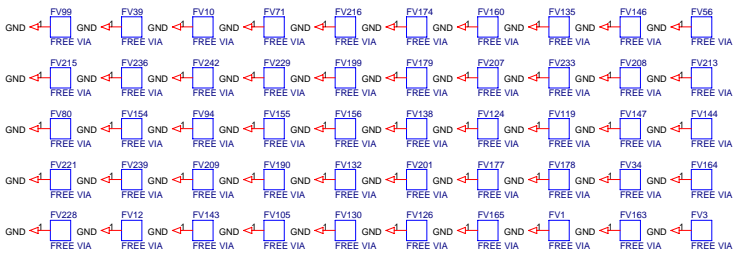
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+5 V Supply



Connect analog and digital grounds in one place to insure they are at the same potential

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