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CLK_ADC_CNTRL

CLK_ADC_EXT

INT_CLK_ON_OFF

DFS

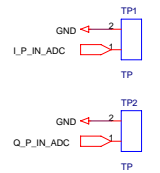
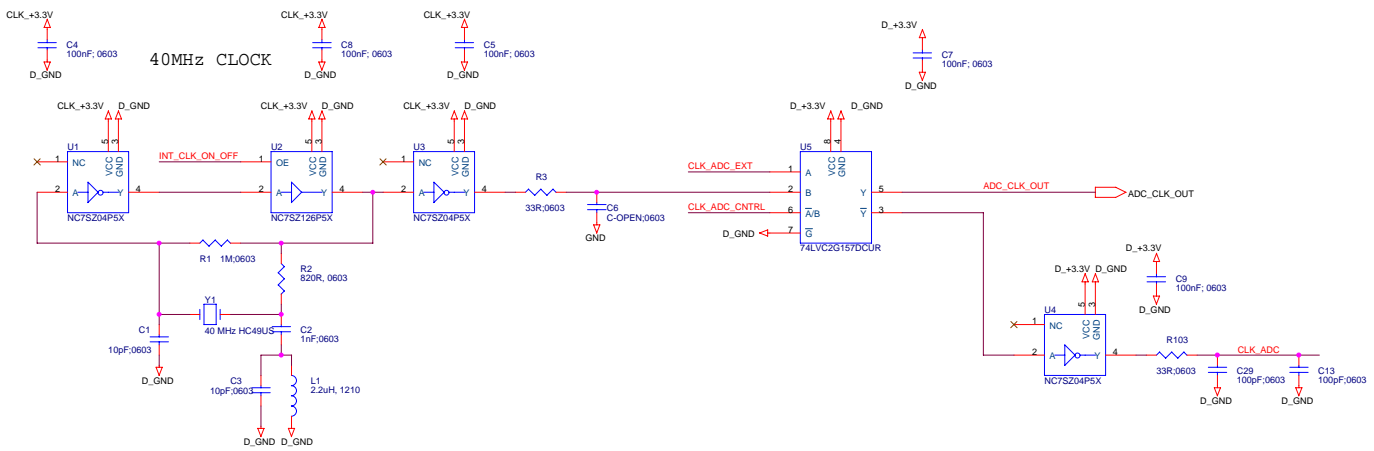
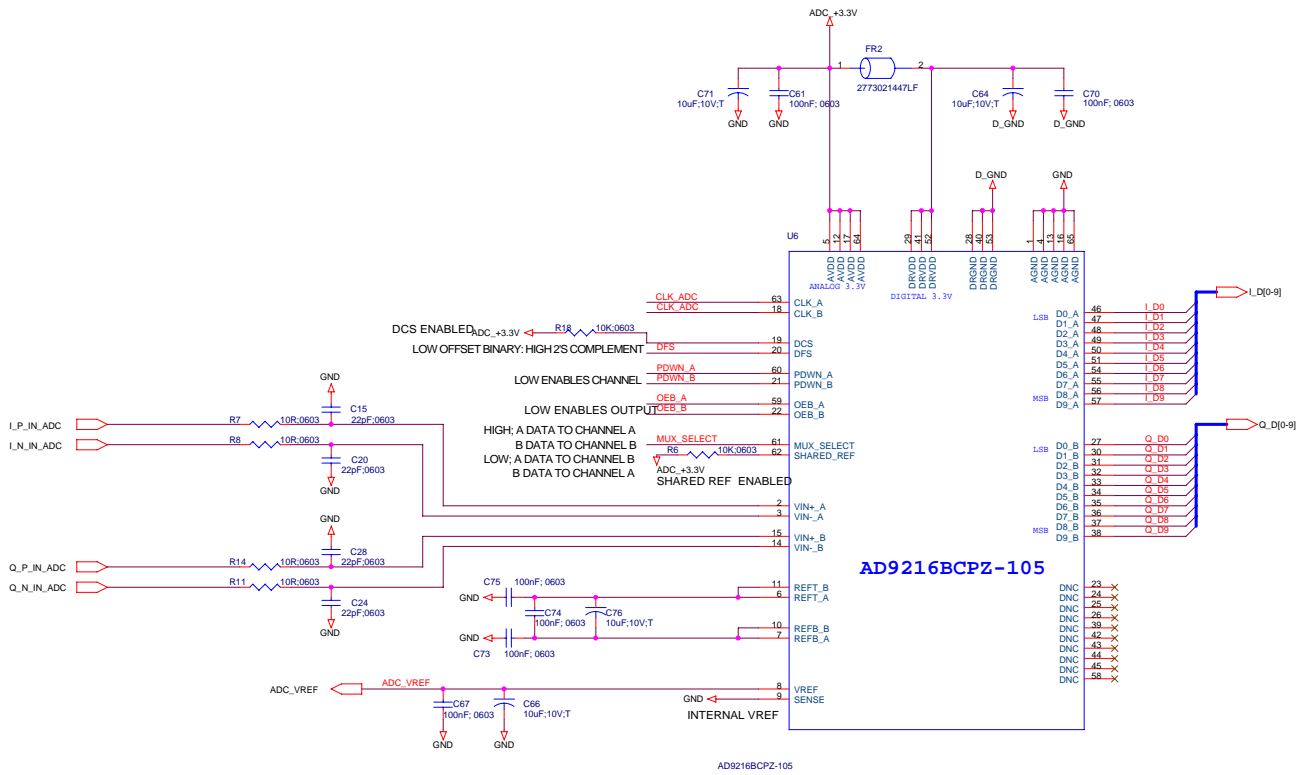
PDWN_A

PDWN_B

OEB_A

OEB_B

MUX_SELECT

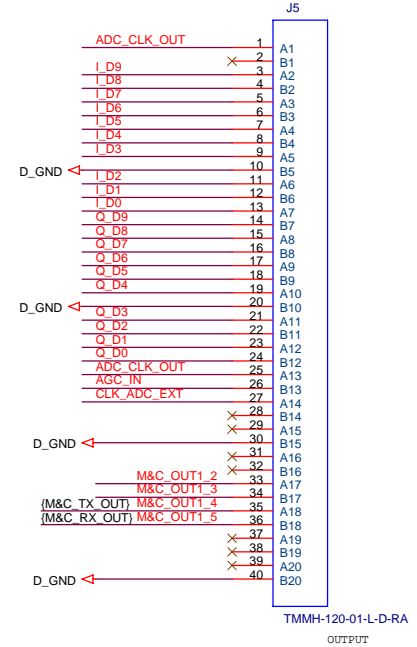
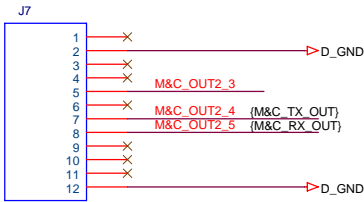
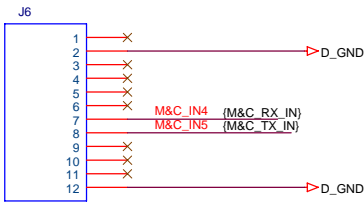
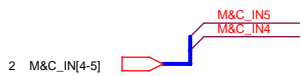
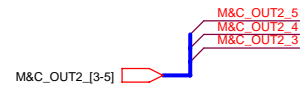
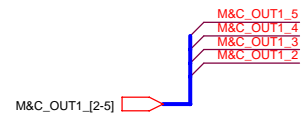
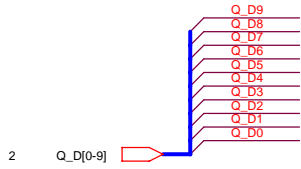
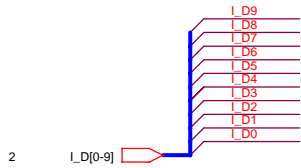


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Michael J. Yoha 18221A Flower Hill Way Cathersburg, MD 20879 USA	
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CLK_ADC_EXT ← CLK_ADC_EXT

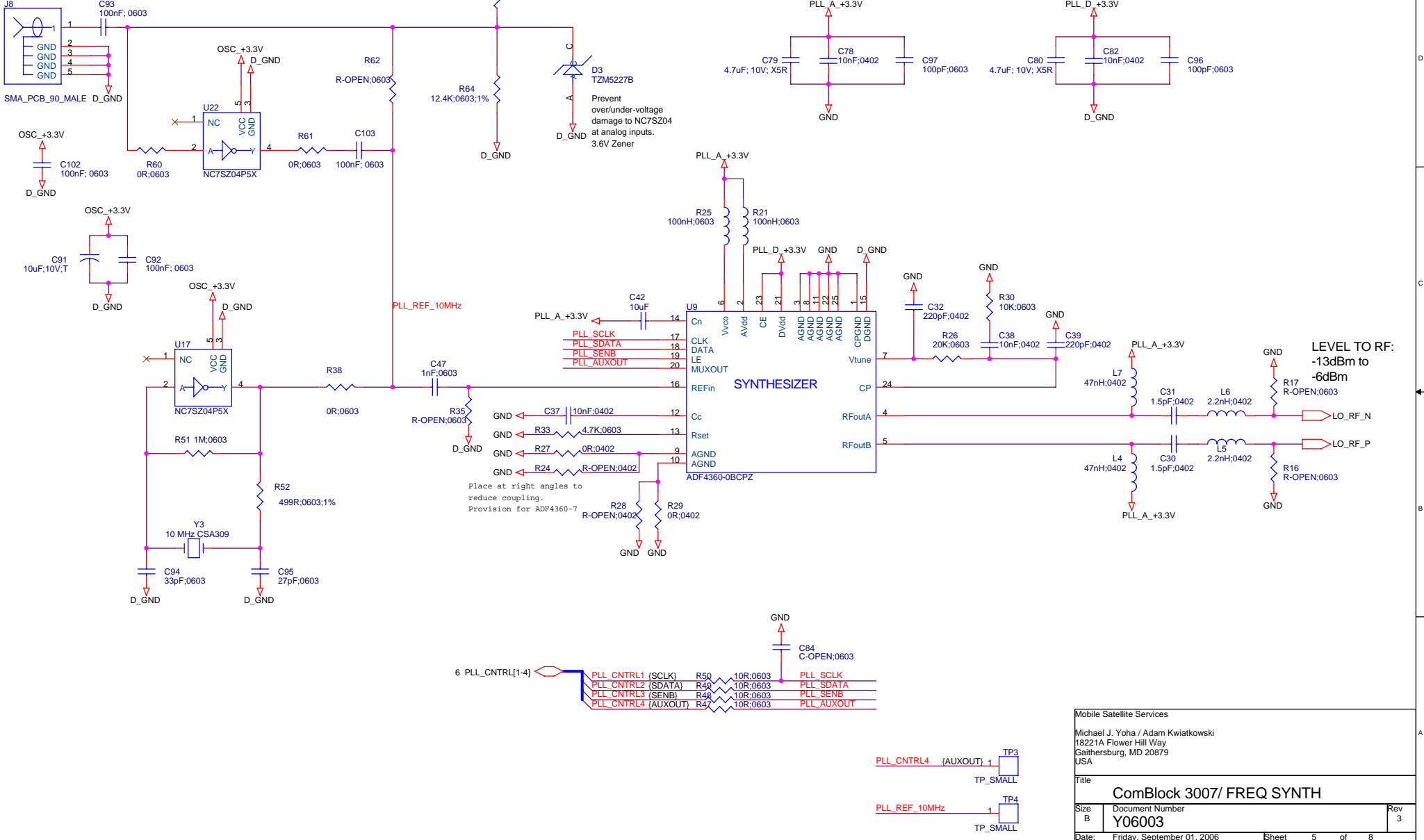
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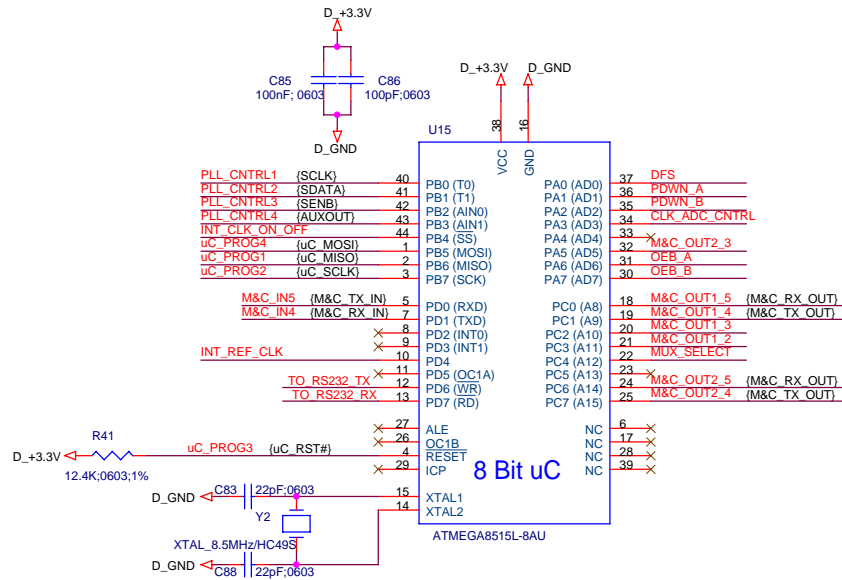
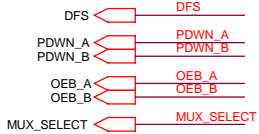
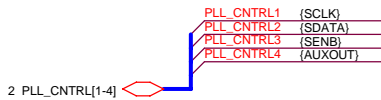
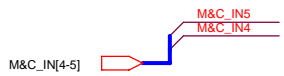
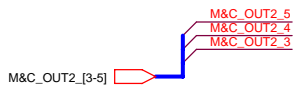
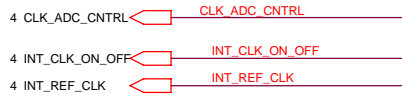
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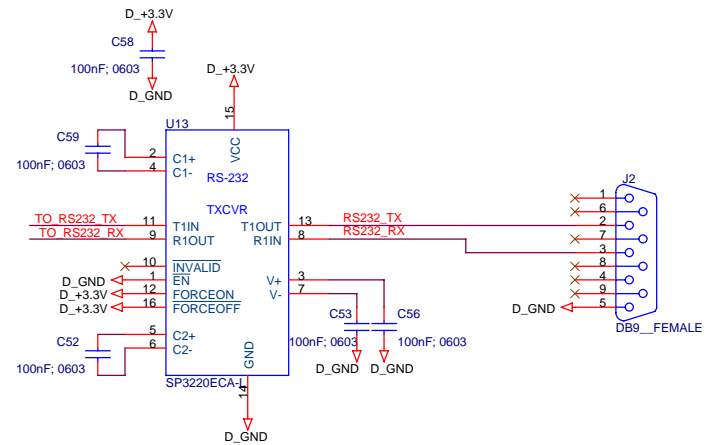
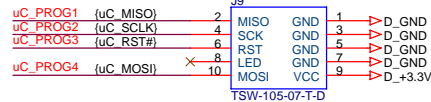
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EXTERNAL FREQUENCY REFERENCE





IN CIRCUIT PROGRAMMING HEADER



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Michael J. Yoha
18221A Flower Hill Way
Gaithersburg, MD 20879
USA

Title
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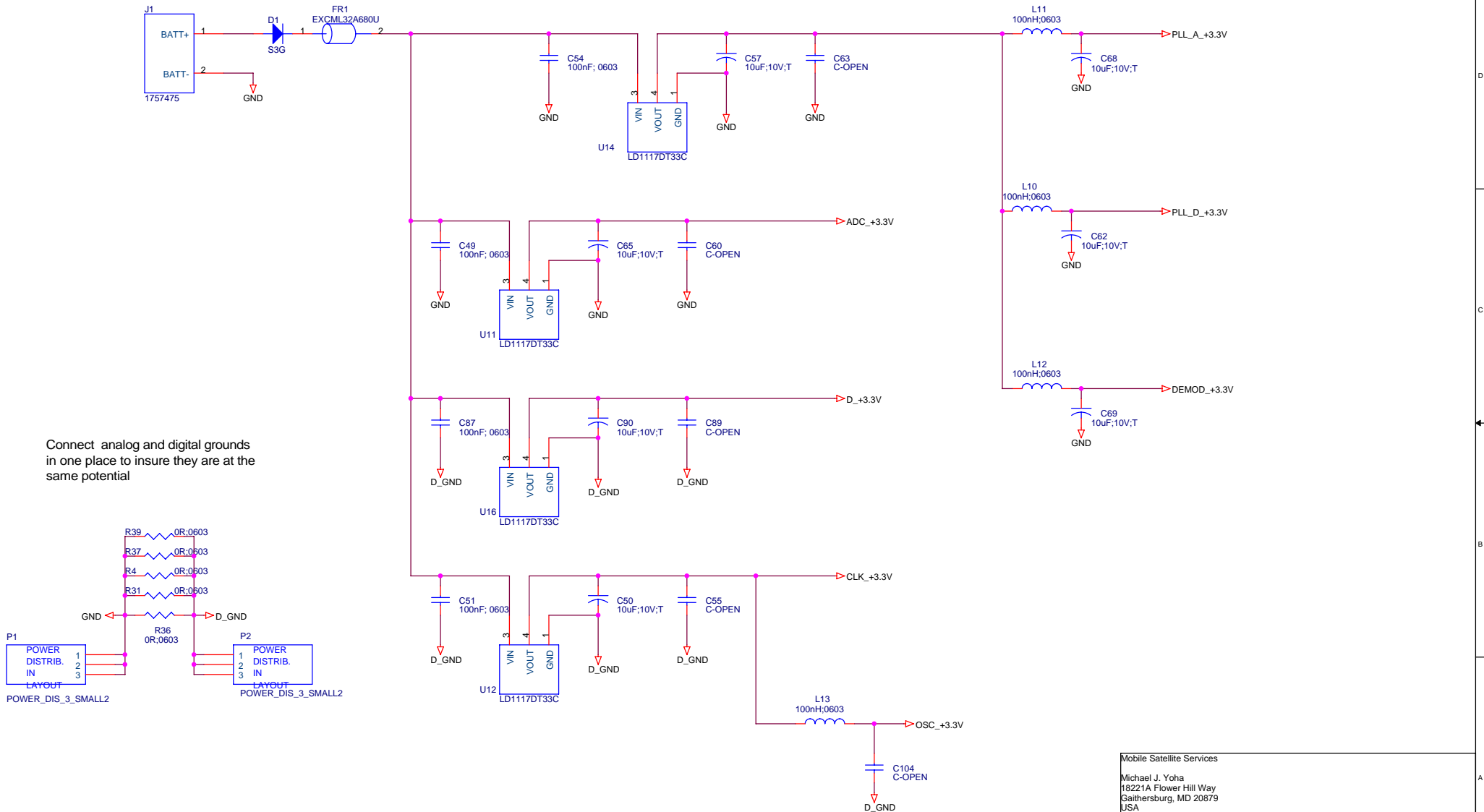
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+5 V Supply



Connect analog and digital grounds in one place to insure they are at the same potential

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