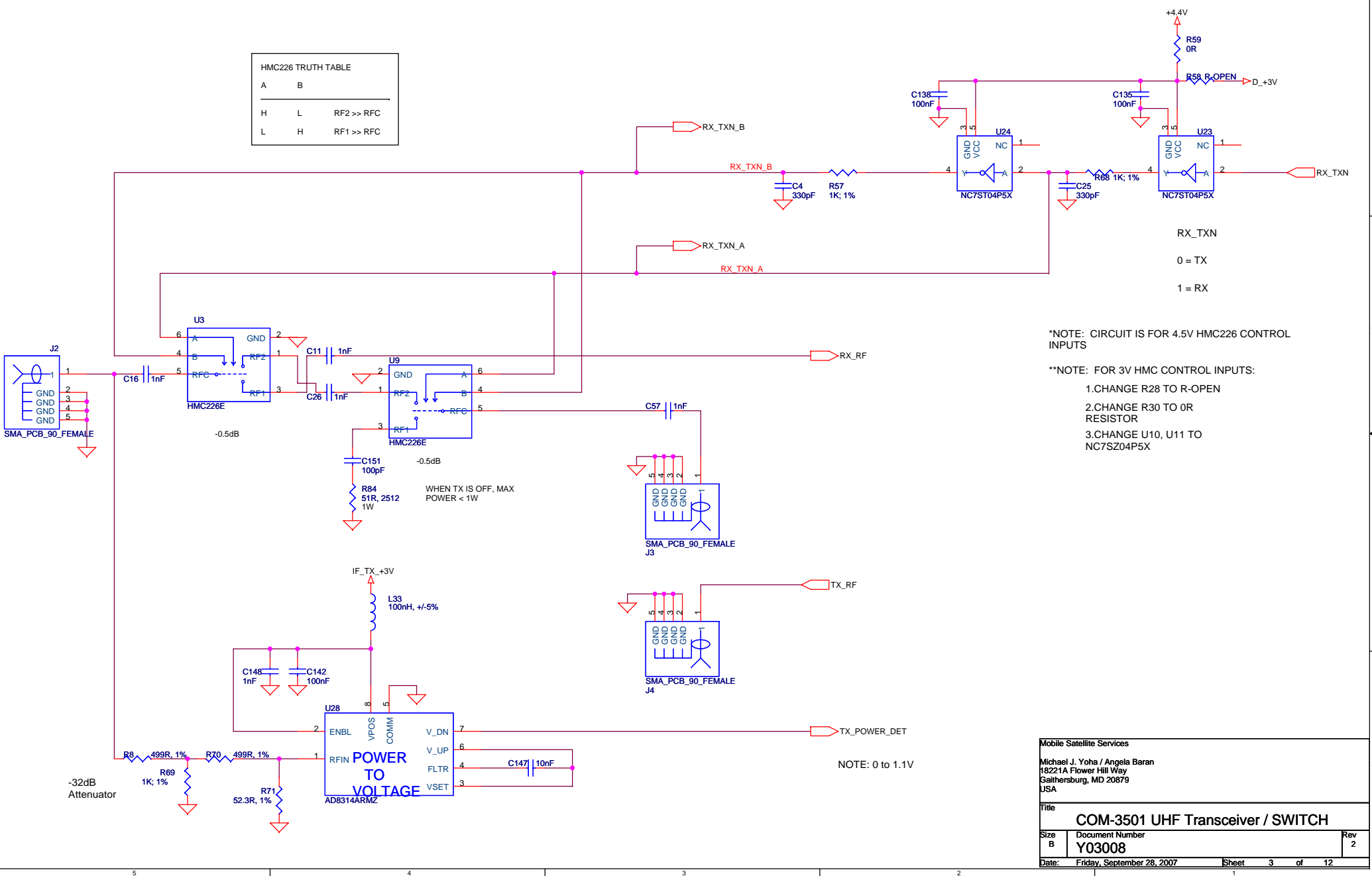


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HMC226 TRUTH TABLE

A	B	
H	L	RF2 >> RFC
L	H	RF1 >> RFC



*NOTE: CIRCUIT IS FOR 4.5V HMC226 CONTROL INPUTS

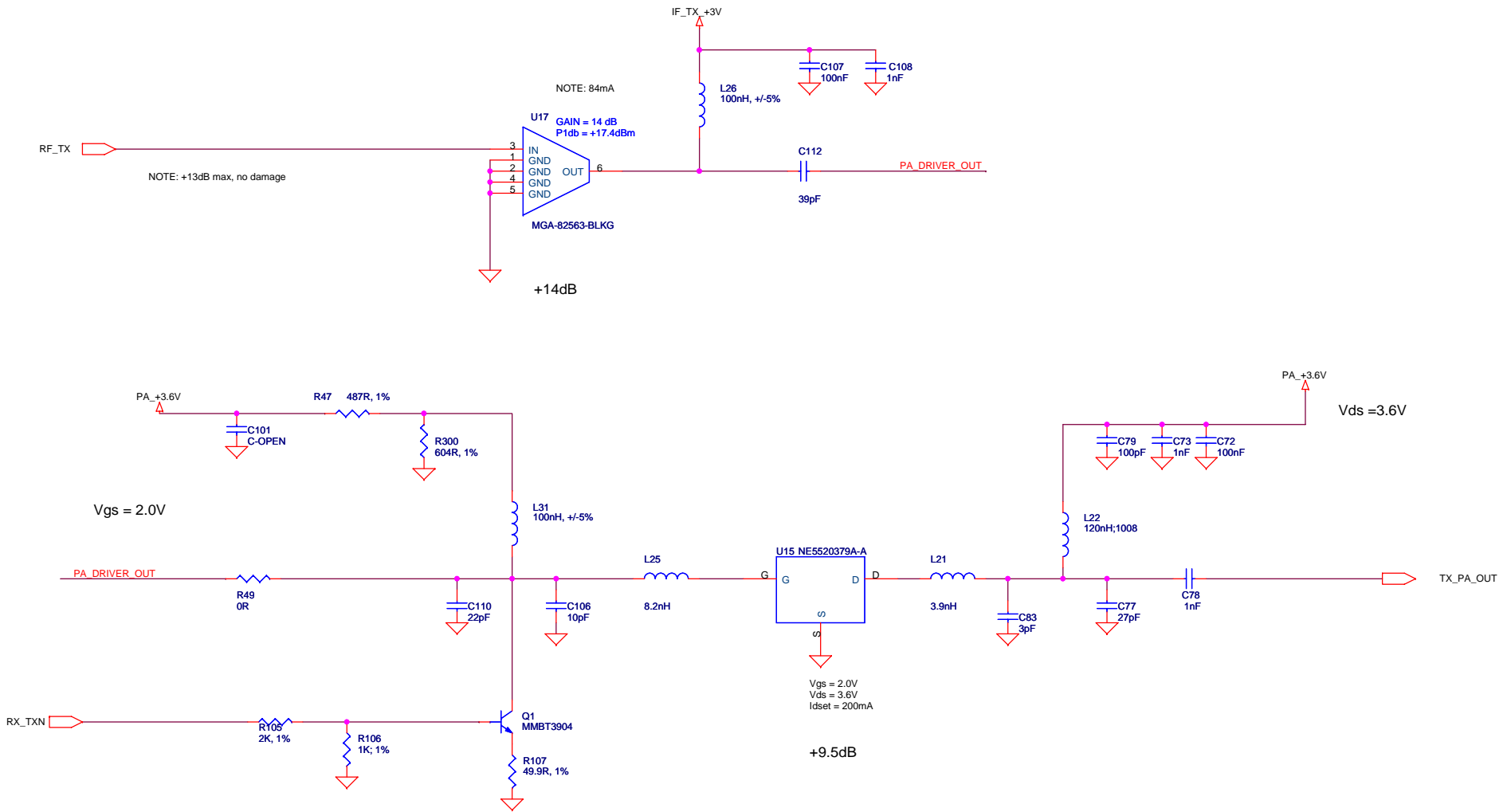
**NOTE: FOR 3V HMC CONTROL INPUTS:

- 1.CHANGE R28 TO R-OPEN
- 2.CHANGE R30 TO 0R RESISTOR
- 3.CHANGE U10, U11 TO NC7SZ04P5X

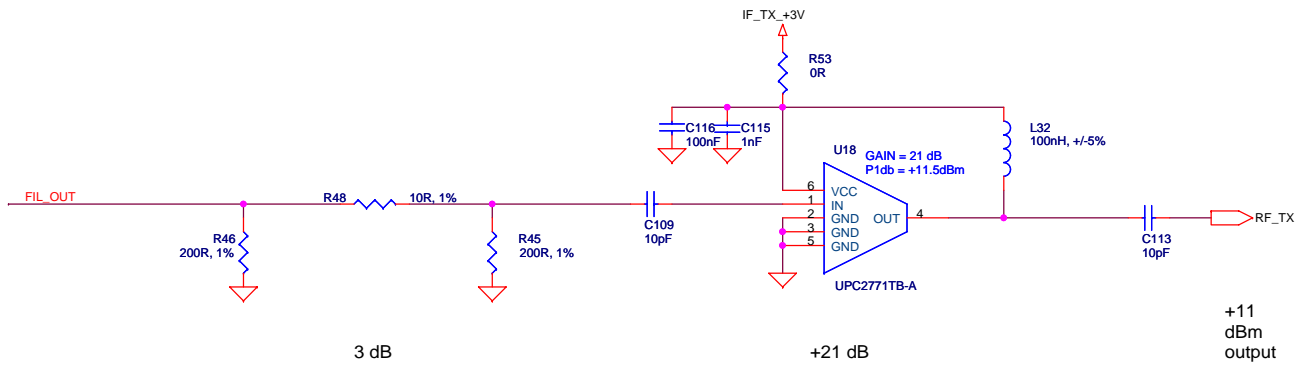
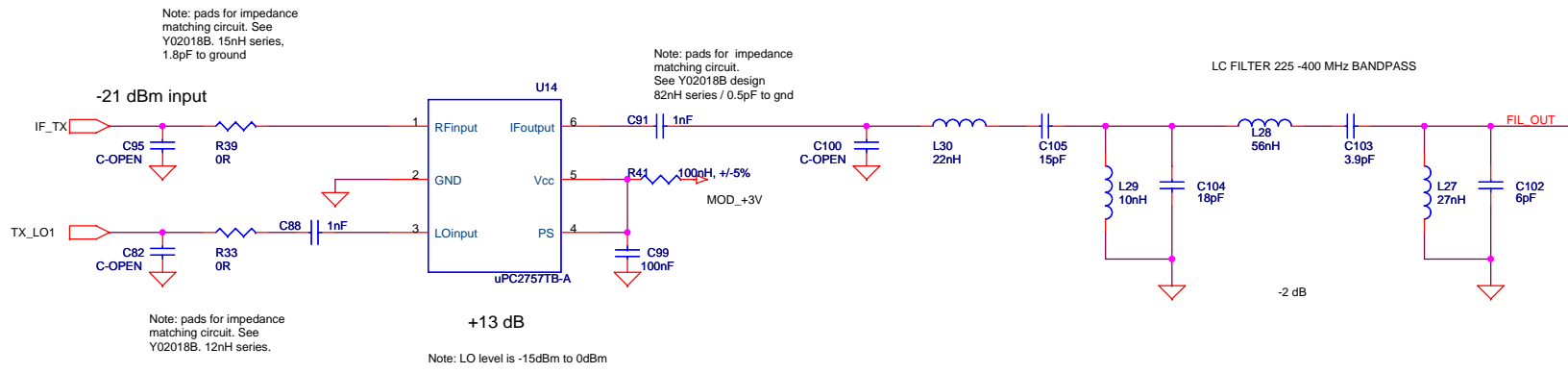
WHEN TX IS OFF, MAX POWER < 1W

NOTE: 0 to 1.1V

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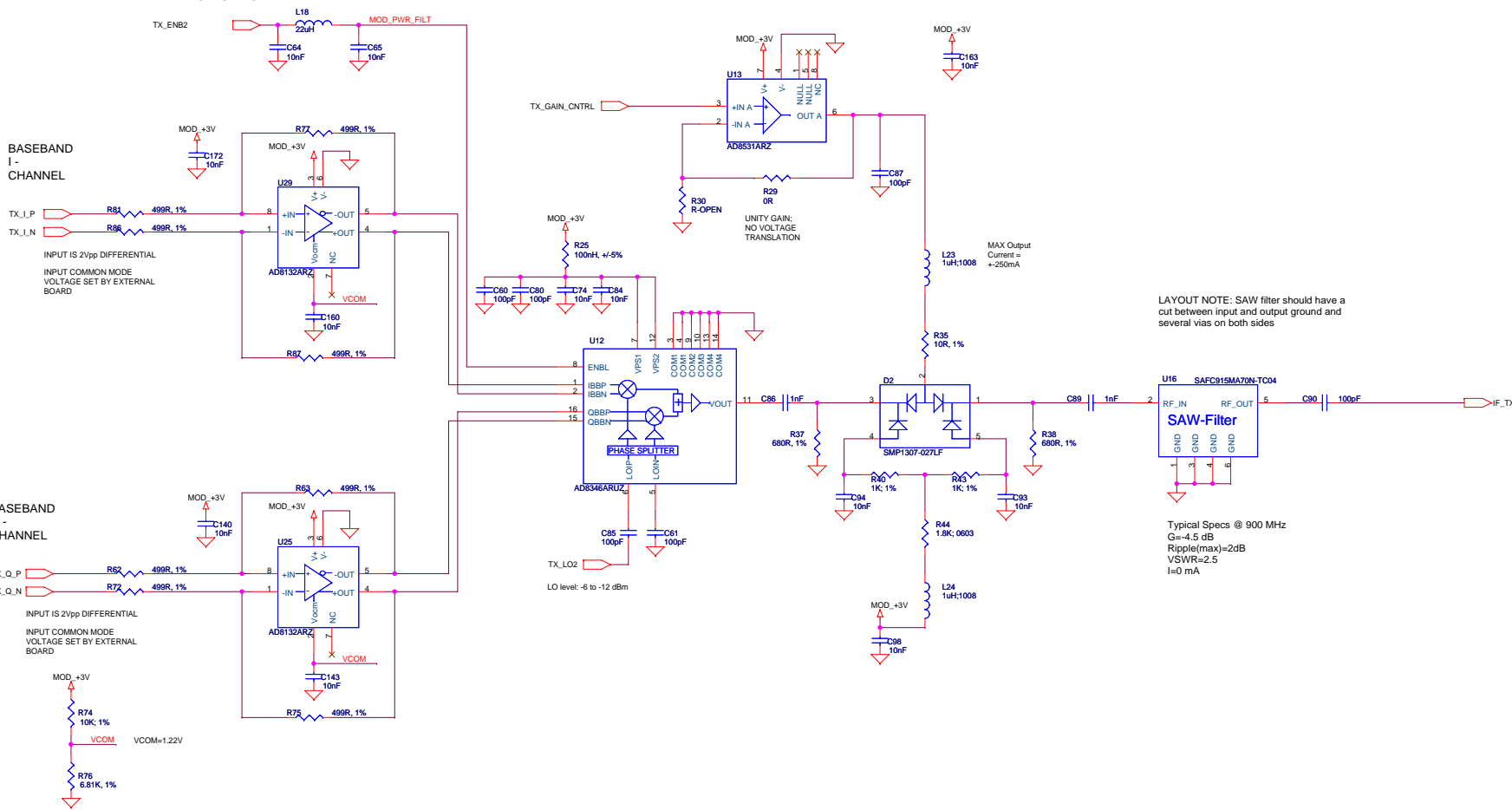


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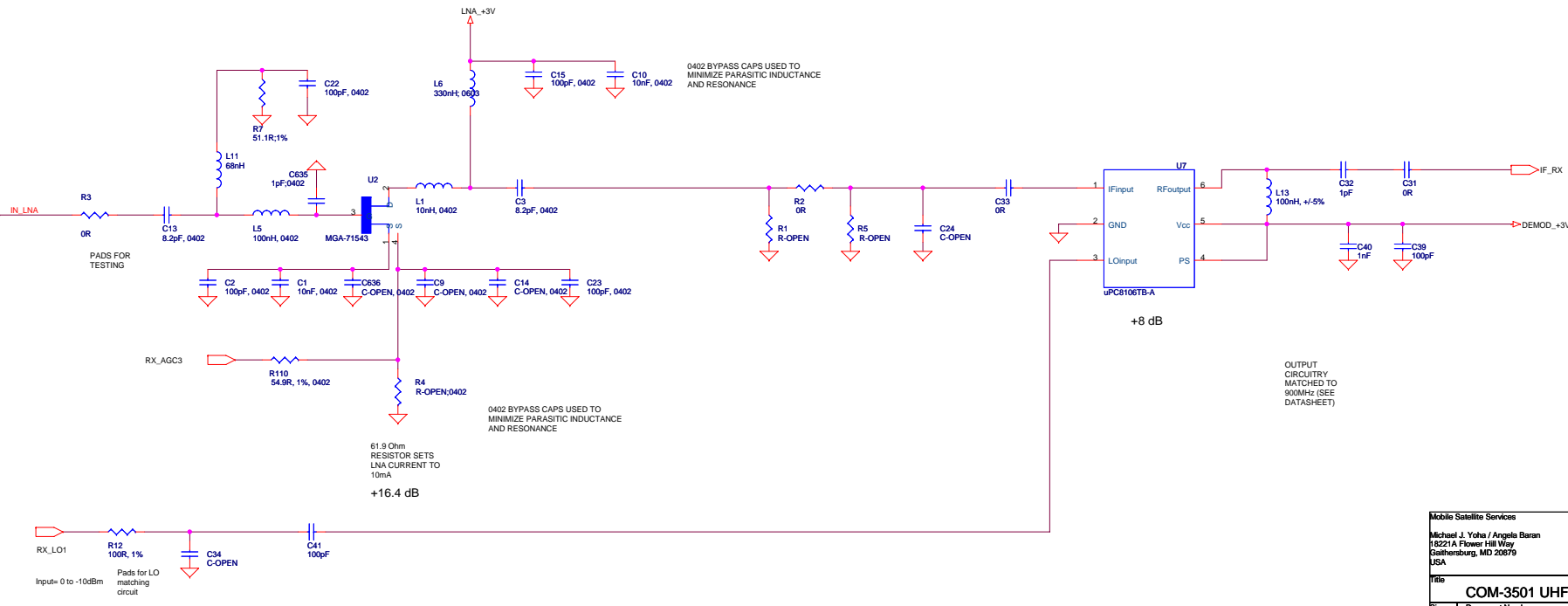
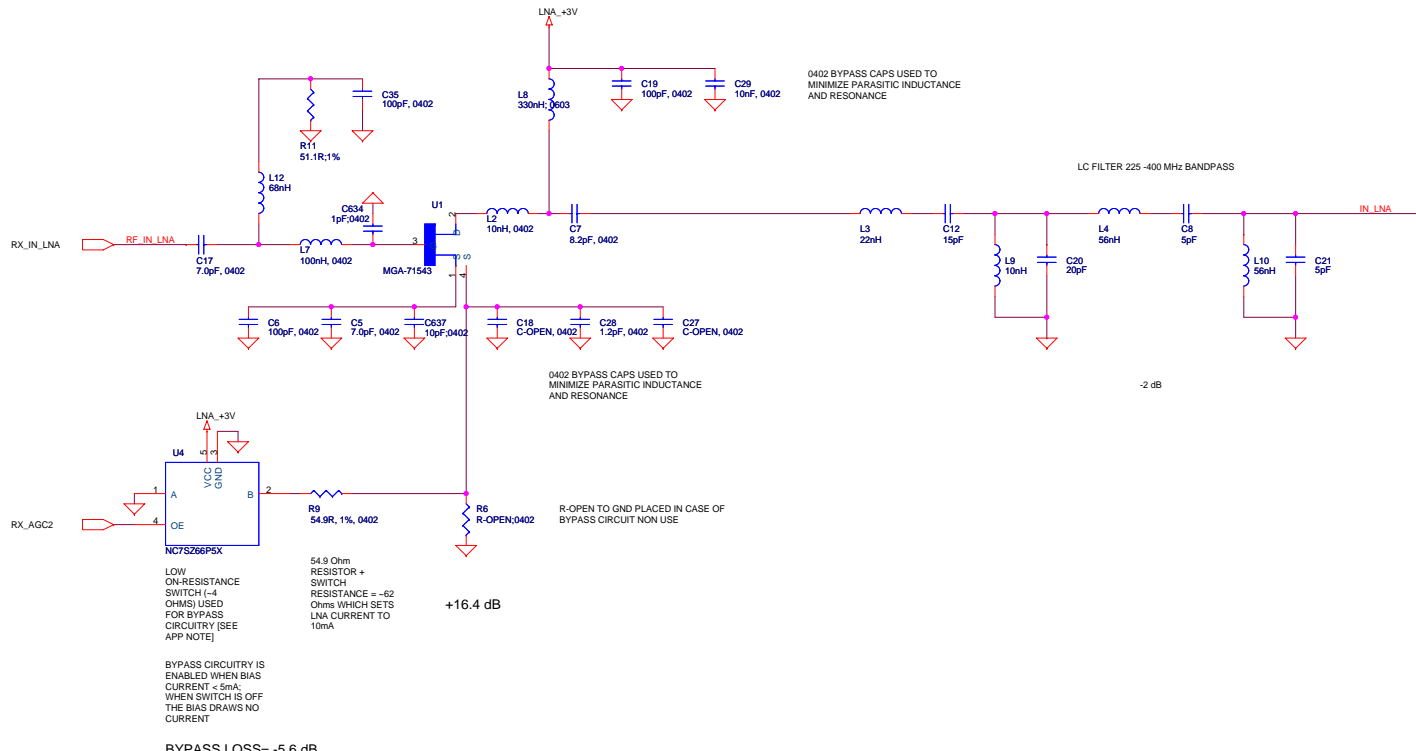
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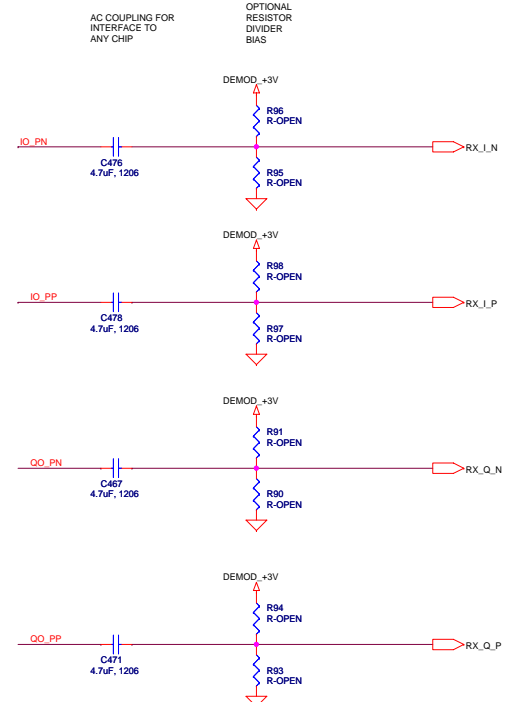
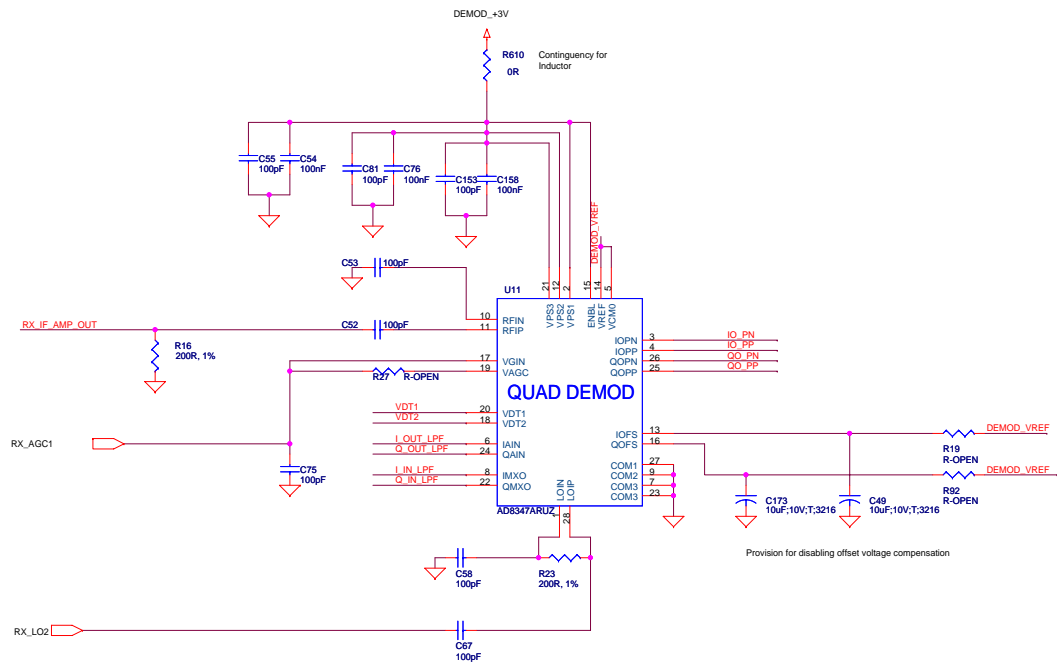
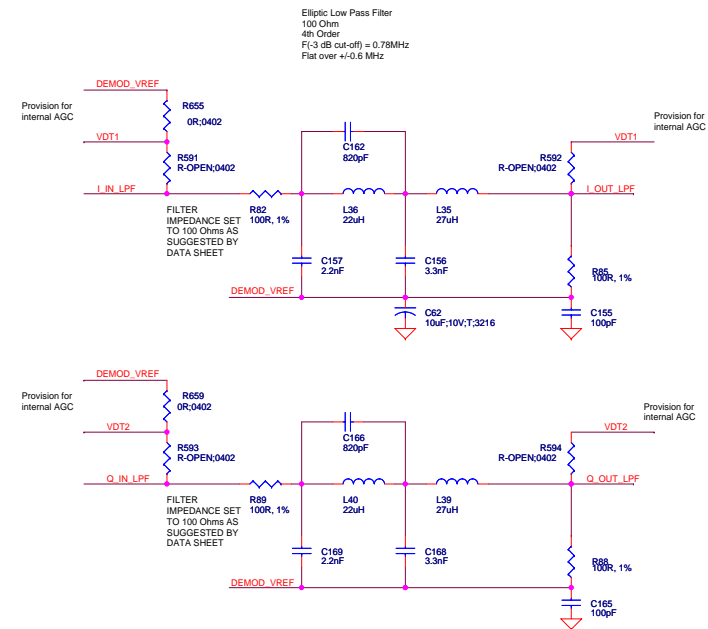
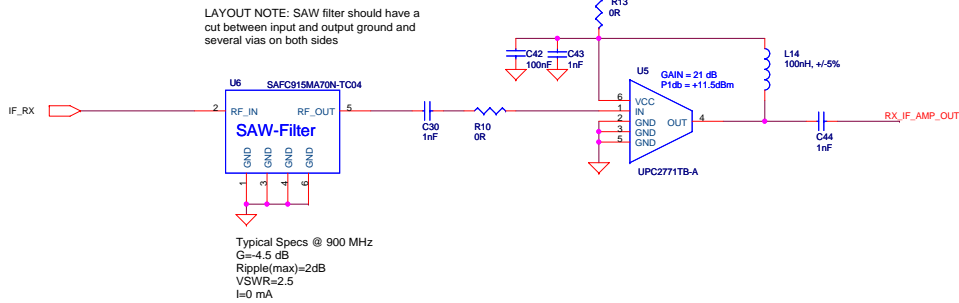
ENABLE IS HIGH ACTIVE

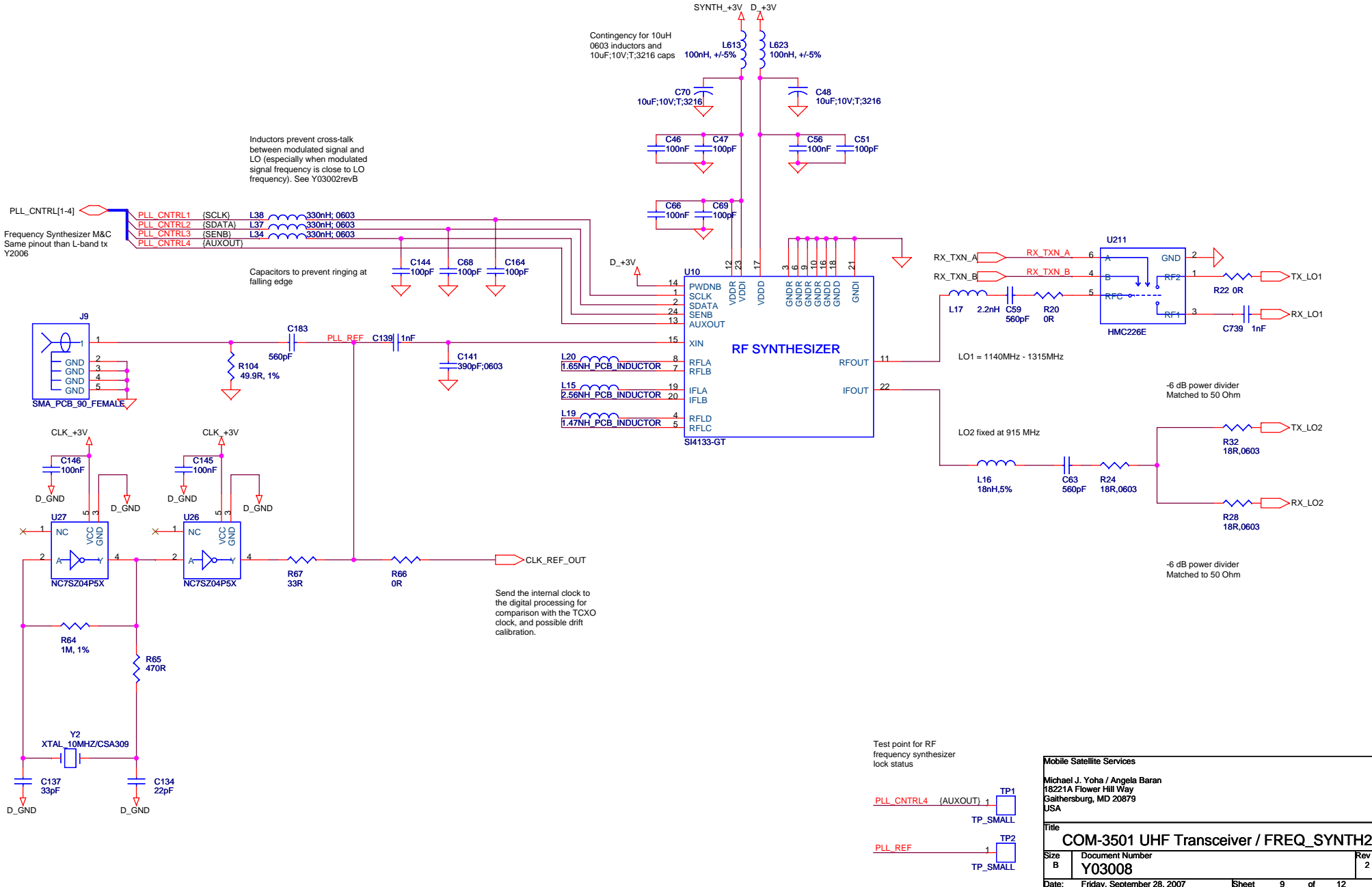


LAYOUT NOTE: SAW filter should have a cut between input and output ground and several vias on both sides

Typical Specs @ 900 MHz
 G=4.5 dB
 Ripple(max)=2dB
 VSWR=2.5
 I=0 mA





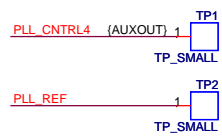


Inductors prevent cross-talk between modulated signal and LO (especially when modulated signal frequency is close to LO frequency). See Y03002revB

Capacitors to prevent ringing at falling edge

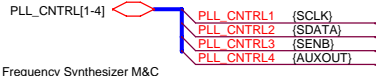
Send the internal clock to the digital processing for comparison with the TCXO clock, and possible drift calibration.

Test point for RF frequency synthesizer lock status

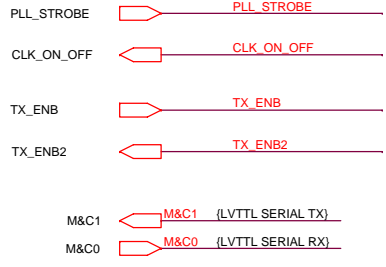


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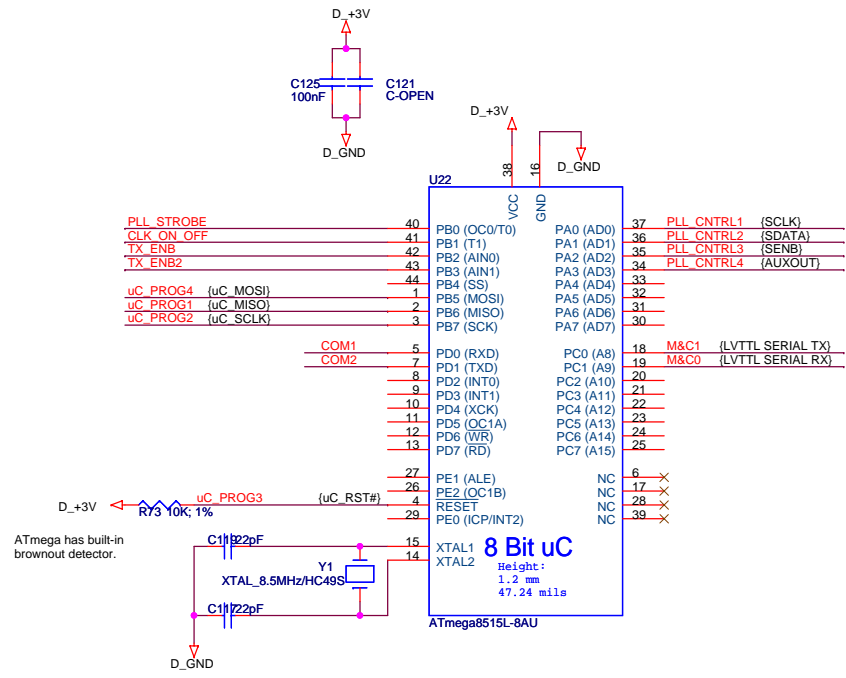
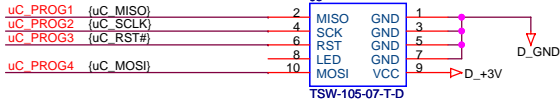
MICROCONTROLLER



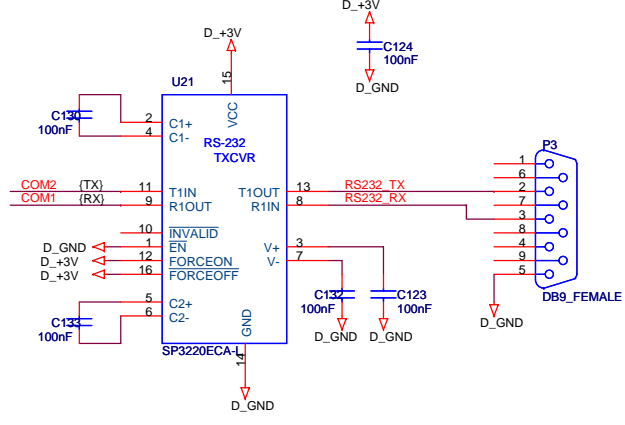
Frequency Synthesizer M&C
 Same pinout than L-band tx
 Y2006



IN CIRCUIT PROGRAMMING HEADER

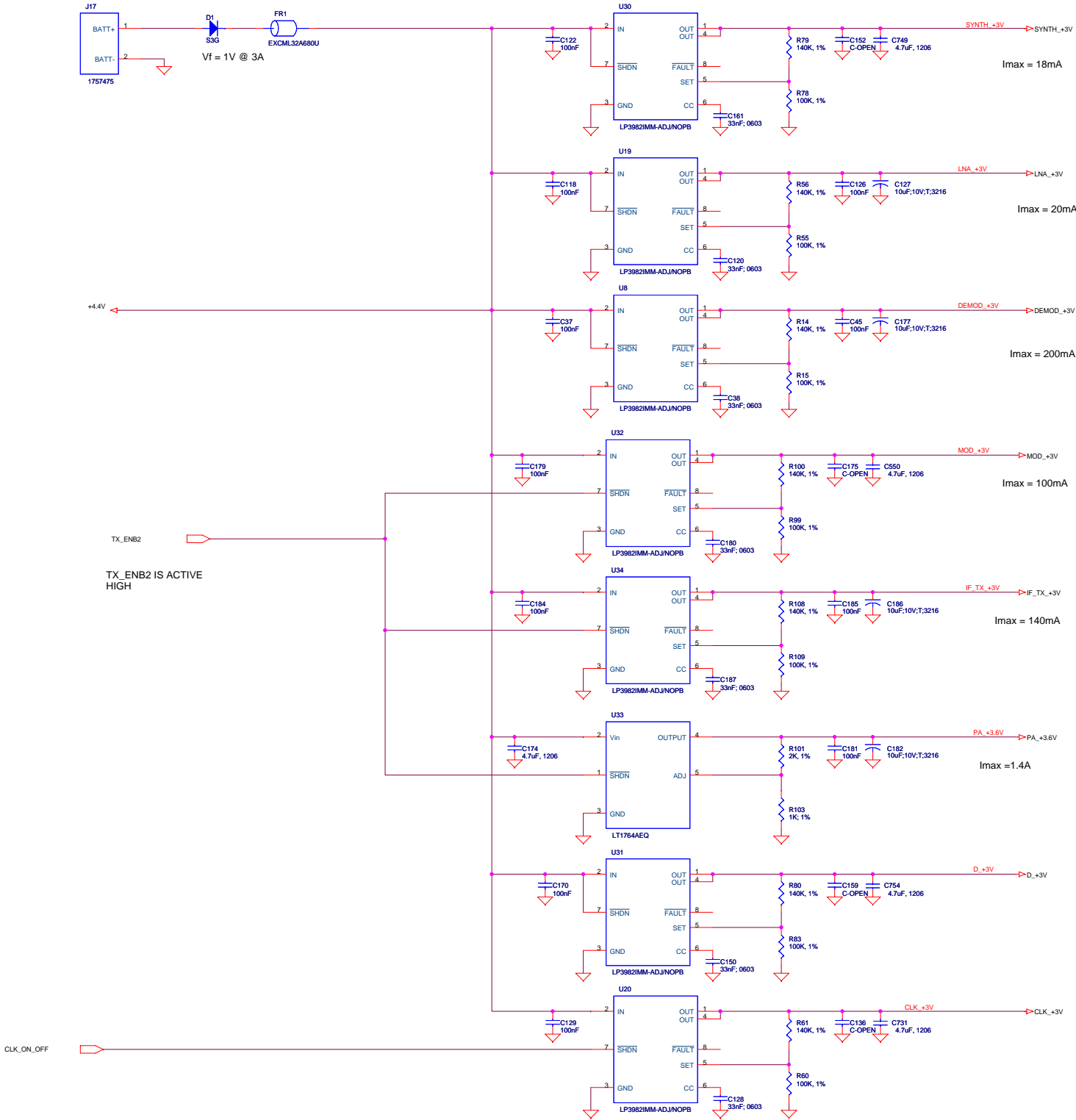
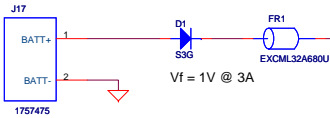


LVTTTL 115.2 Kbaud/s serial communication with digital board



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+5 V Supply



Imax = 18mA

Imax = 20mA

Imax = 200mA

Imax = 100mA

Imax = 140mA

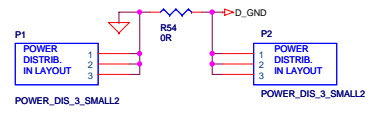
Imax = 1.4A

Itotal = 1.9A

TX_ENB2
TX_ENB2 IS ACTIVE HIGH

CLK_ON_OFF

Connect analog and digital grounds in one place to insure they are at the same potential



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