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Mobile Satellite Services

Christian K. Föhr
 18221A Flower Hill Way
 Gaithersburg, MD 20879
 USA

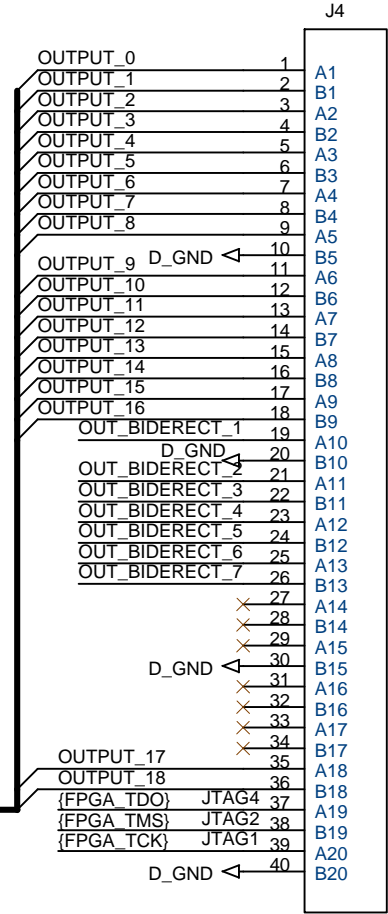
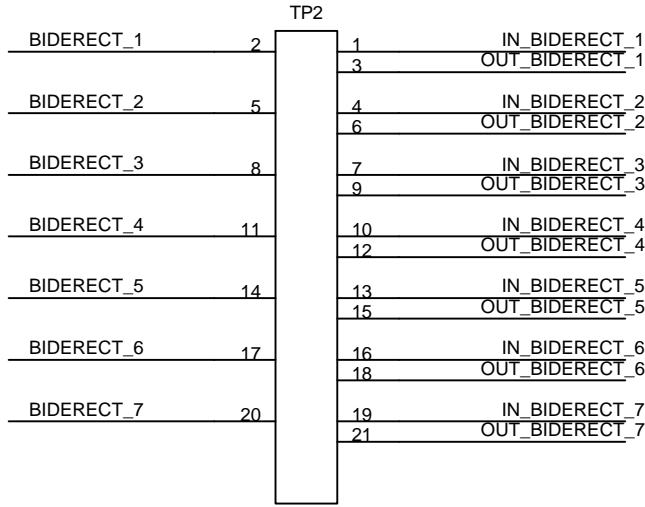
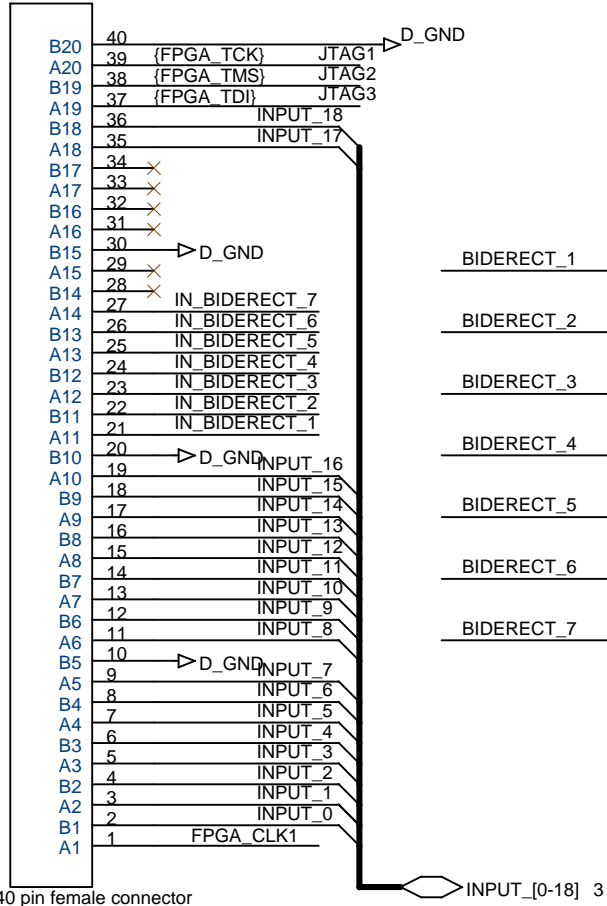
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40 Pin INPUT - Left

40 Pin OUTPUT



7P/3R/M_TM

3 OUTPUT_[0-18]

3 FPGA_CLK1

3 JTAG[1-4]

{FPGA_TDO} JTAG4
 {FPGA_TDI} JTAG3
 {FPGA_TMS} JTAG2
 {FPGA_TCK} JTAG1

3 BIDIRECT_[1-7]

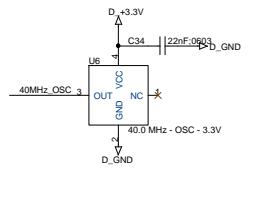
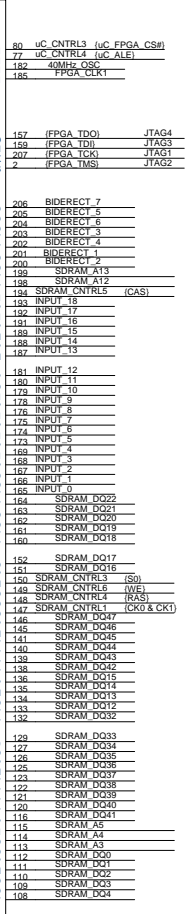
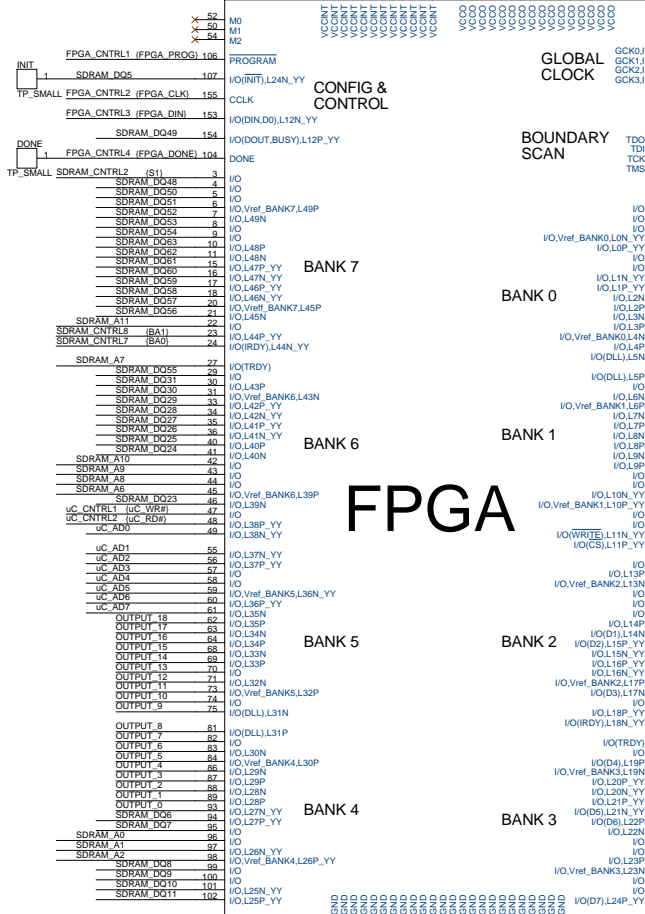
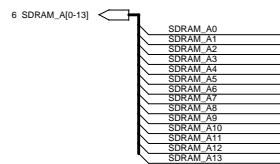
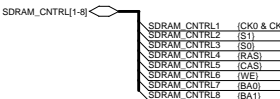
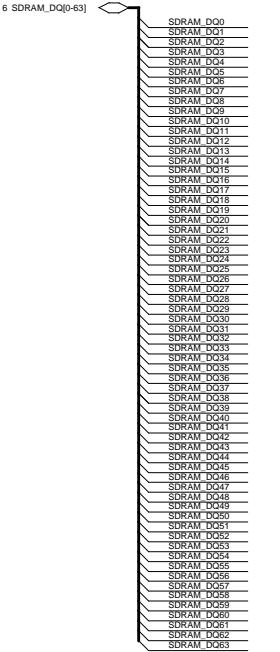
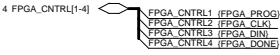
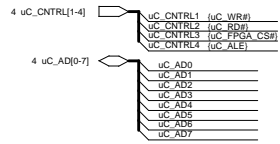
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 BIDIRECT_7

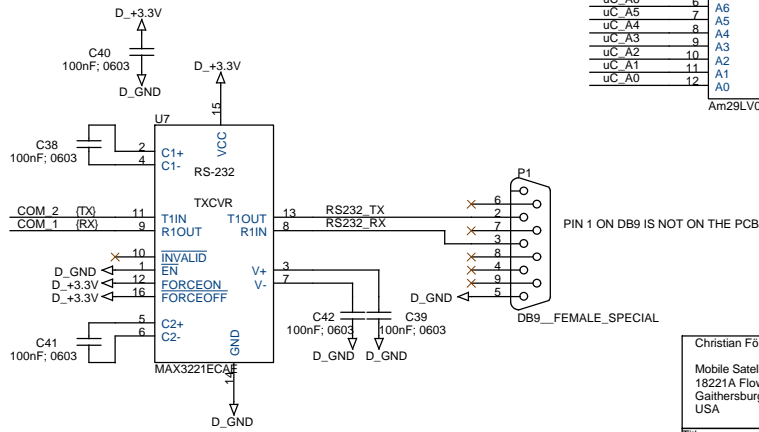
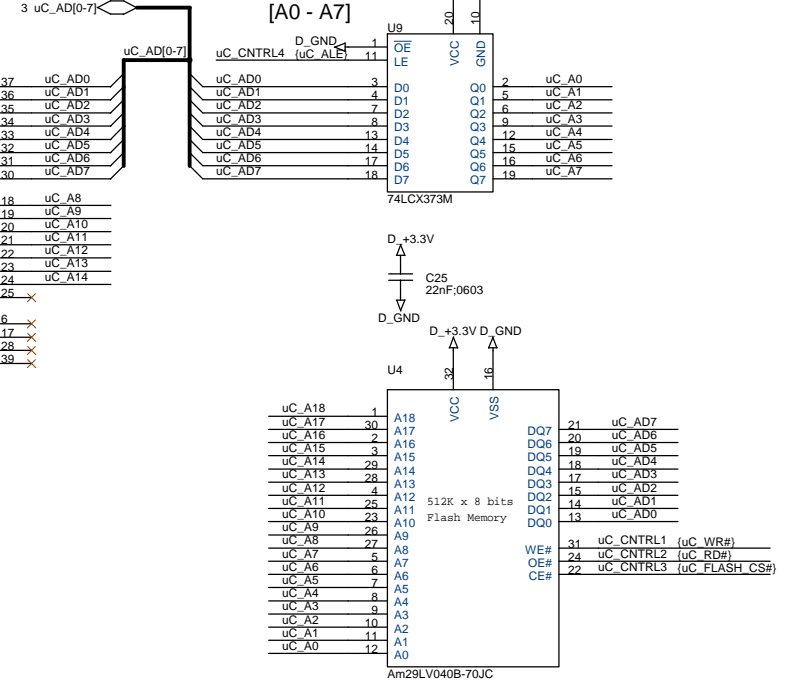
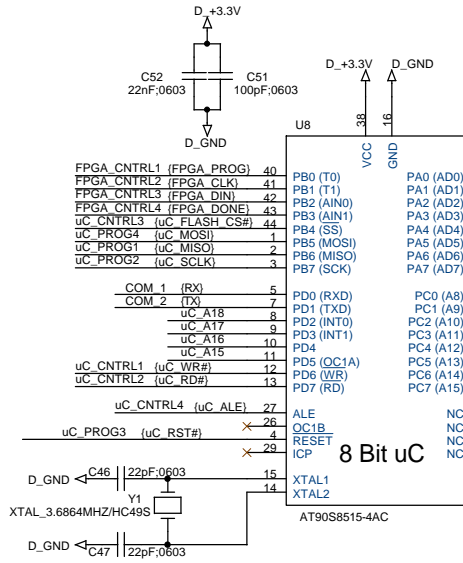
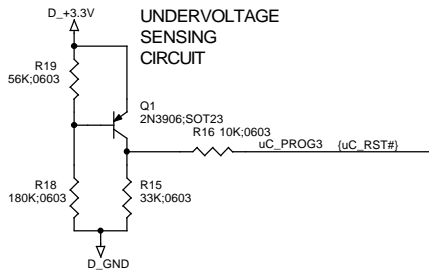
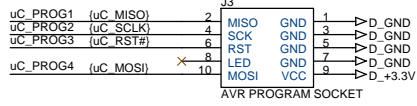
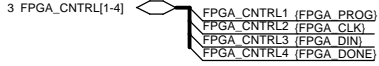
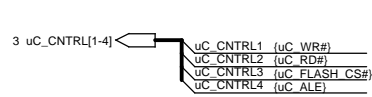
Christian Föhr
 Mobile Satellite Services
 18221A Flower Hill Way
 Gaithersburg, MD 20879
 USA

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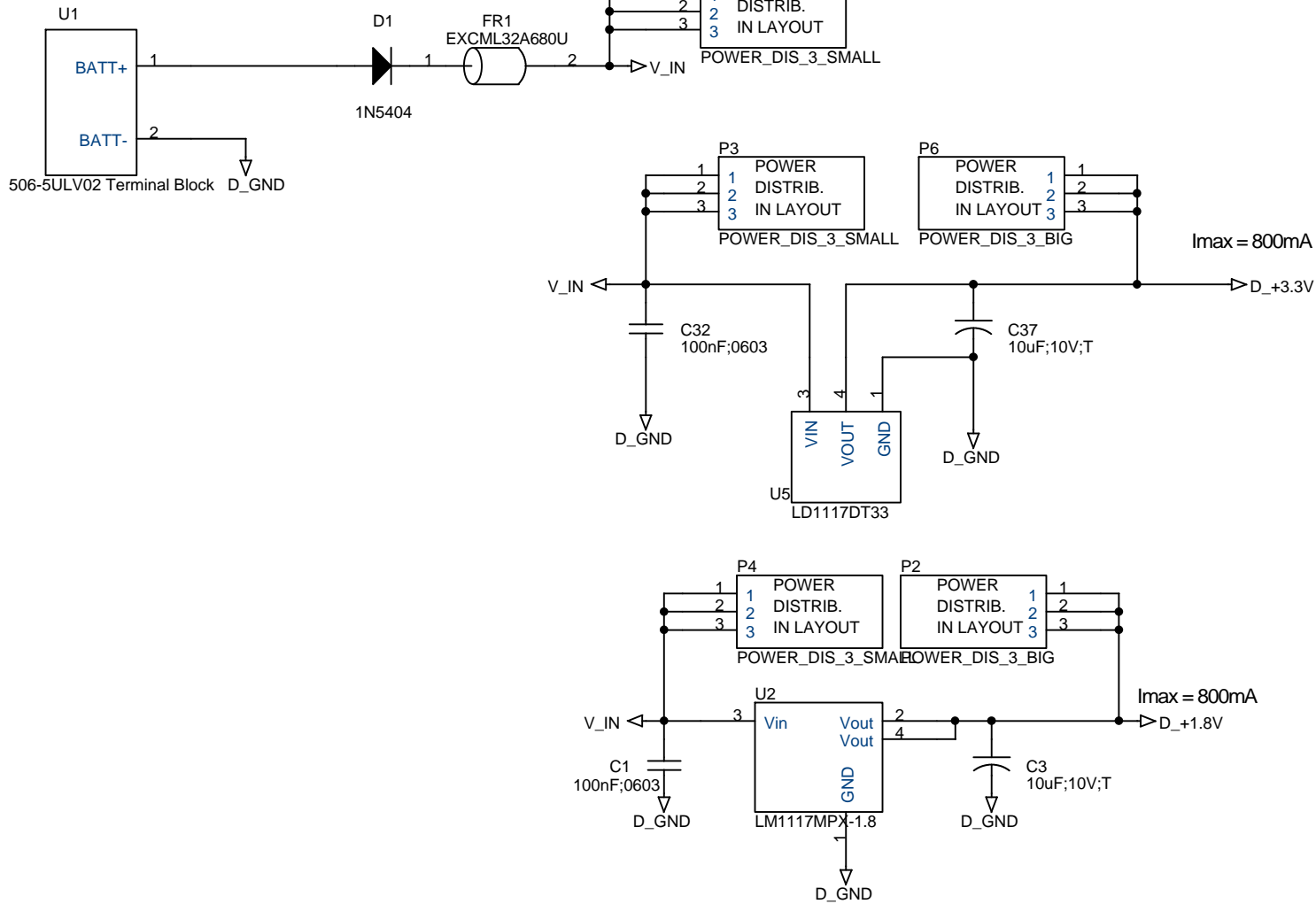
Christian Föhr
 Mobile Satellite Services
 18221A Flower Hill Way
 Gaithersburg, MD 20879
 USA

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+5 V Supply

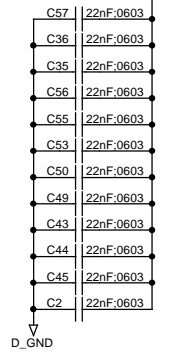
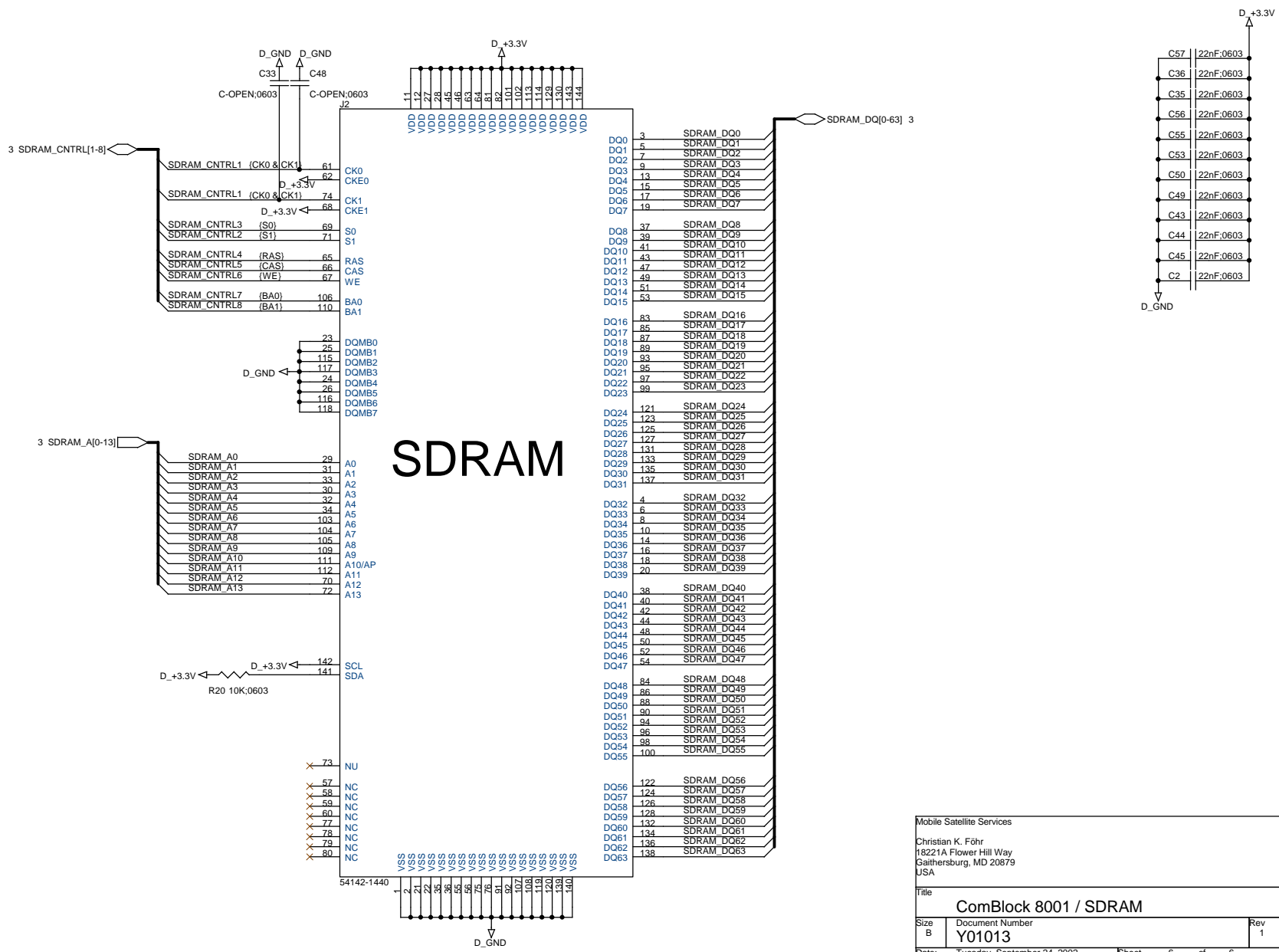


Christian Föhr
 Mobile Satellite Services
 18221A Flower Hill Way
 Gaithersburg, MD 20879
 USA

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Christian K. Föhr 18221A Flower Hill Way Gaithersburg, MD 20879 USA		
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