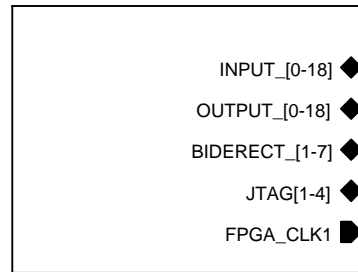
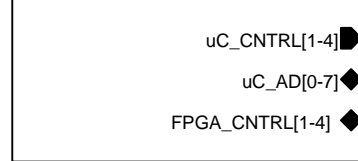


CONNECTOR



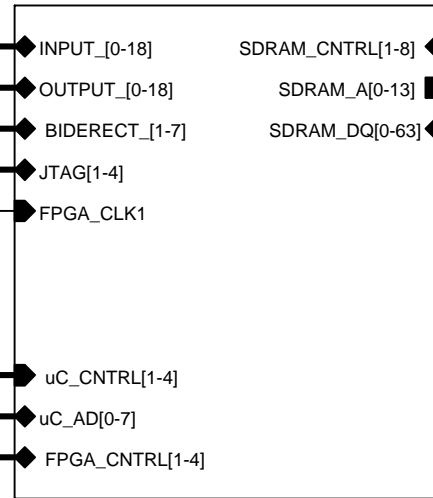
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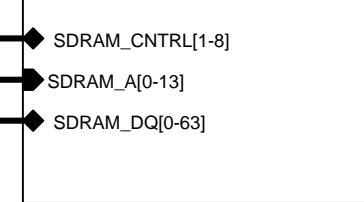
MICRO

FPGA



FPGA

SDRAM



SDRAM

POWER



POWER

TABLE OF CONTENTS

MAIN.....1	MICRO.....4
CONNECTOR.....2	POWER.....5
FPGA.....3	SDRAM.....6

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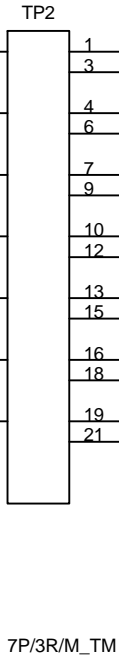
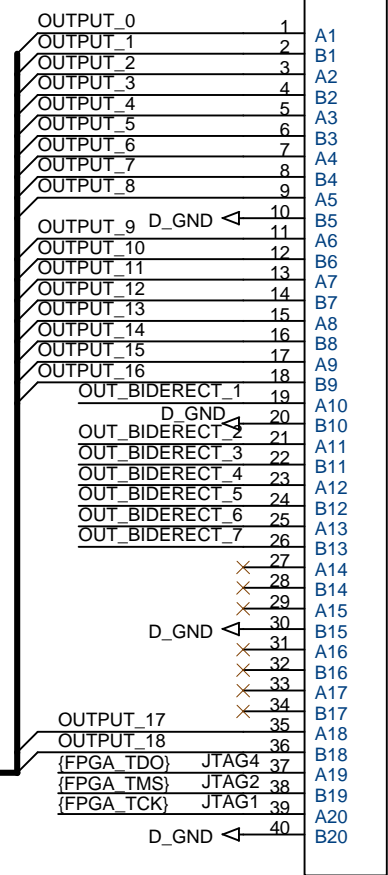
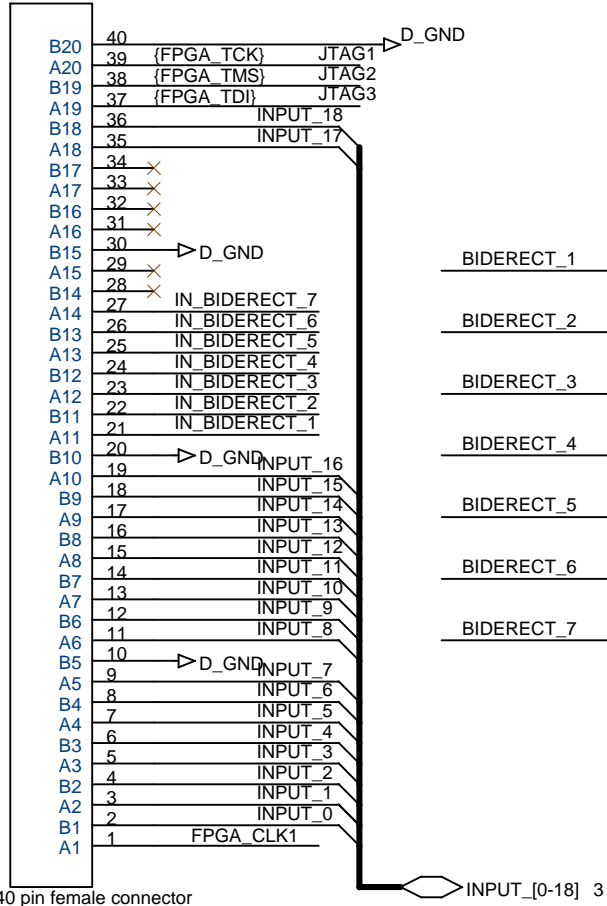
Rev 1

Date: Tuesday, September 24, 2002

Sheet 1 of 6

# 40 Pin INPUT - Left

# 40 Pin OUTPUT

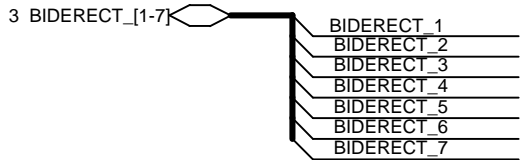
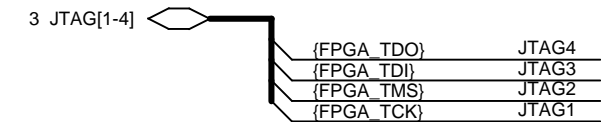
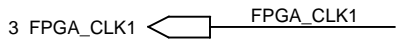


3 OUTPUT\_[0-18]

7P/3R/M\_TM

2mm 40 pin male connector

2mm 40 pin female connector

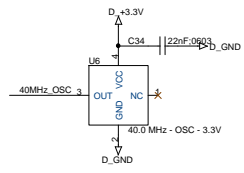
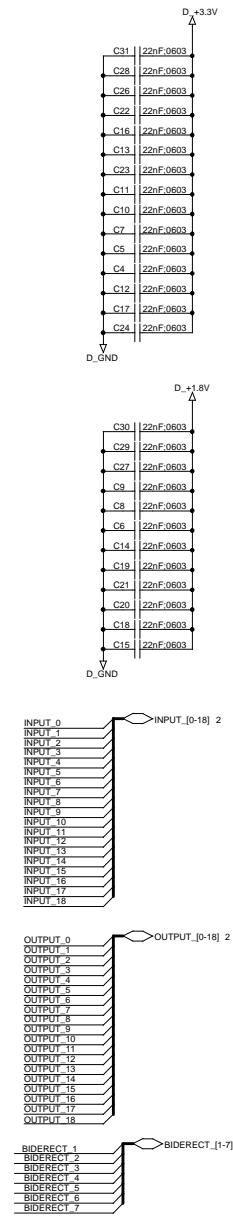
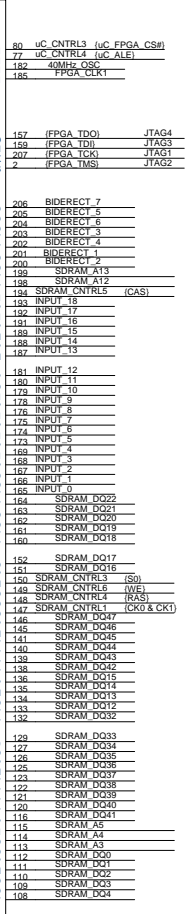
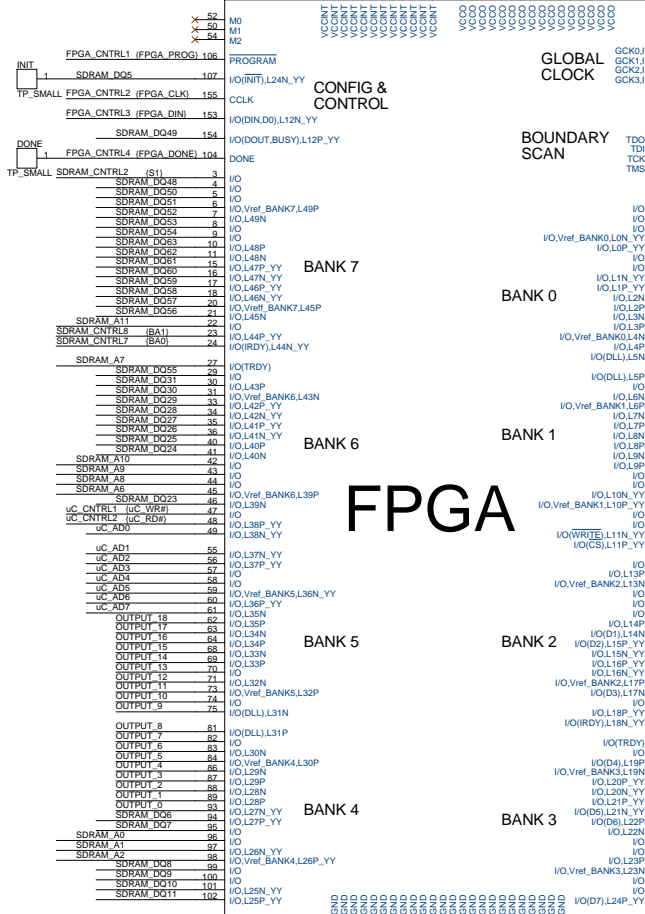
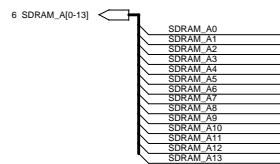
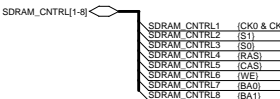
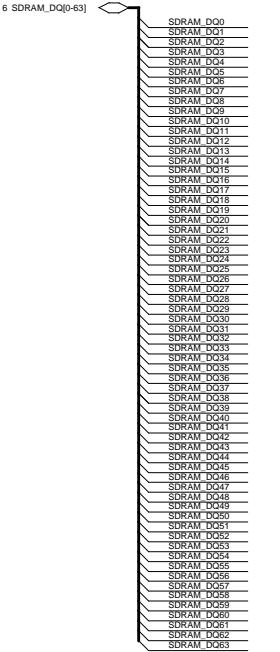
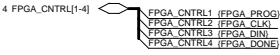
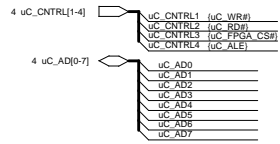


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Date: Tuesday, September 24, 2002 Sheet 2 of 6



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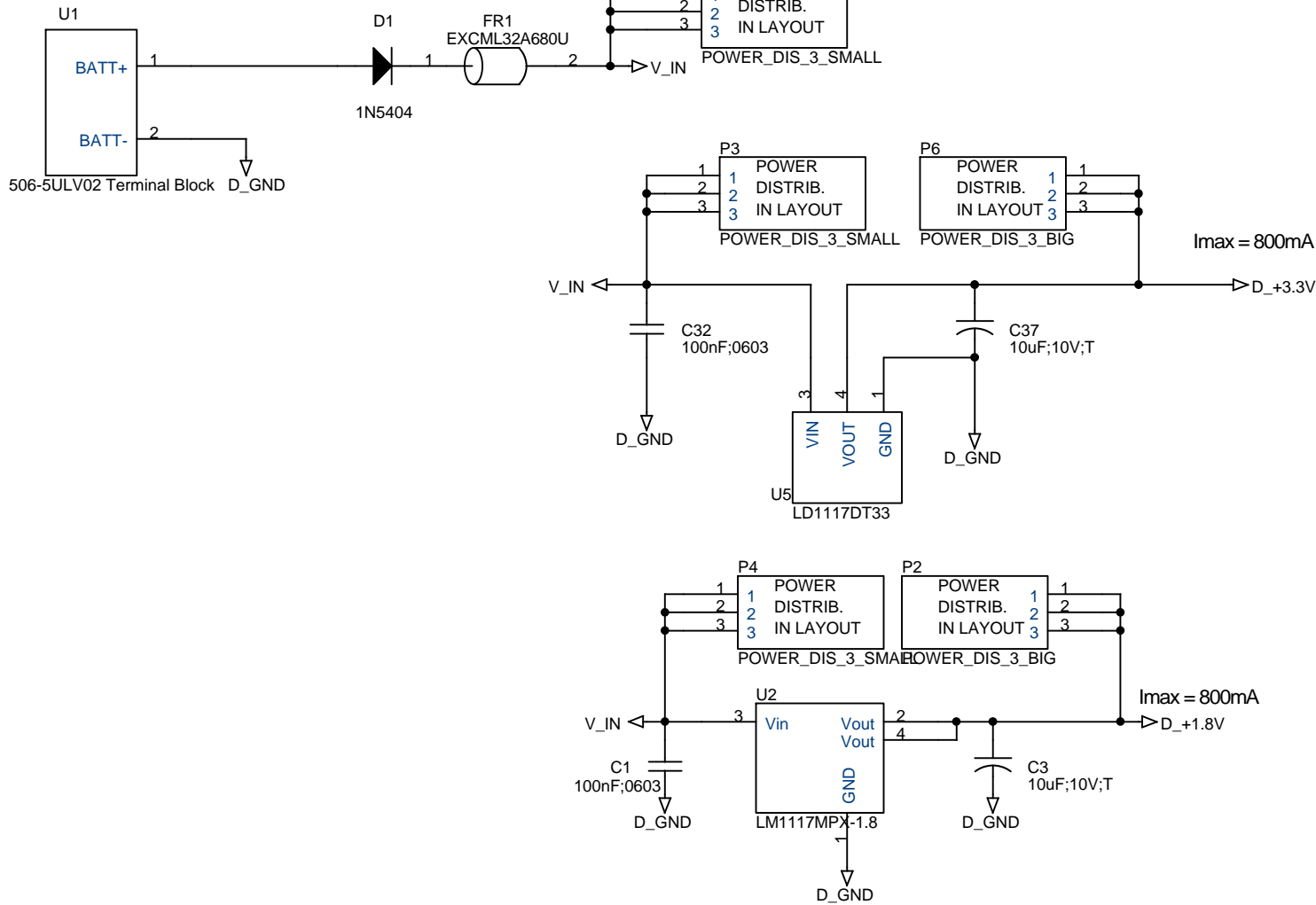
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Size C Document Number  
**Y01013**

Date: Tuesday, September 23, 2008 Sheet 3 of 6



# +5 V Supply

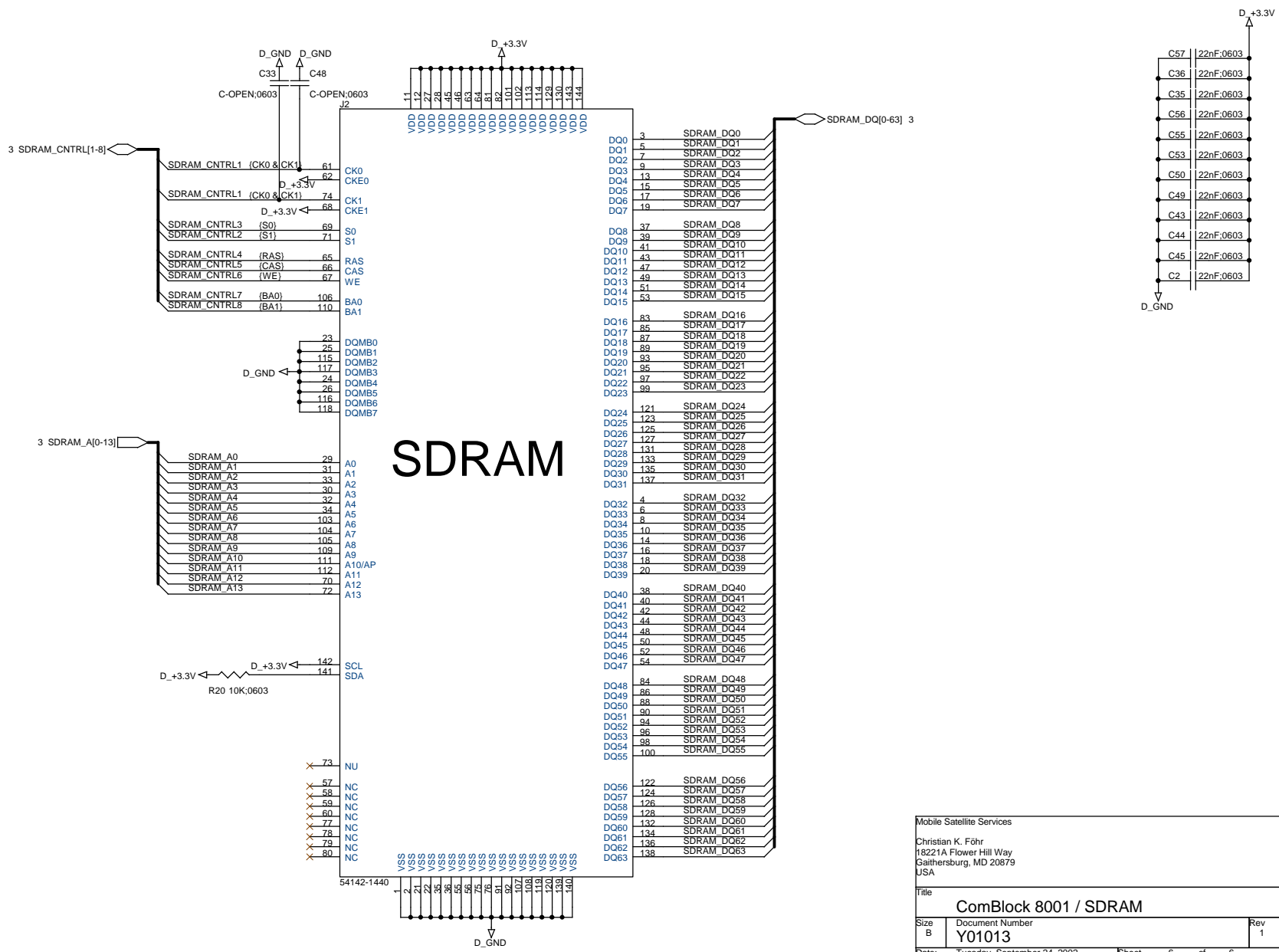


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Size A	Document Number <b>Y01013</b>	Rev 1
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Date: Tuesday, September 24, 2002      Sheet 5 of 6



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Title <b>ComBlock 8001 / SDRAM</b>		
Size B	Document Number <b>Y01013</b>	Rev 1
Date: Tuesday, September 24, 2002	Sheet 6	of 6