3009 -> 8002 -> 5003

a) Configure the COM-3009 at 40 Msamples/s. DDR output is off. Two jumpers must be inserted, one facing L3, the other facing L10. The jumper J1 should be installed in position 1-2

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COM5003 TCP-IP / USB GATEWAY				
COM8002 High-Speed Data Acquisition				
COM3009 Dual Analog to Digital Converter				
COM 3009 Dual Analog to Digital Converter Basic S 🗙				
	ADC Sampling Frequency: 40000000 Hz			
	✓ Internal ADC Sampling Clock			
	DDR Output Interface			
	Apply Ok Advan Cancel			

b) COM-8002 configuration

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AGC AGC gain Action	Enabled 129 Start Upload	Dec V			
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c) COM-5003 configuration

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COM5003 TCP-IP / USB GATEWAY				
COM3009 Dual Analog to Digital Converter				
COM5003 TCP-IP / USB GATEWAY Basic Settings				
IP-address:				
172.16.1.128				
Input Format:				
8-bit wide from J5				
Output Format:				
Output Disabled				
Apply Ok Advanced Cancel				

A few check points:

the overall power consumption of the complete assembly is 1050mA under 5VDC.
 using ComScope, one can visualize the input signals as illustrated below

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COM5003 TCP-IP / USB GATEWAY	
COM8002 High-Speed Data Acquisition	
COM3009 Dual Analog to Digital Converter	
ComScope, COM8002 High-Speed Data Acquisition	
Trace Signal Representation Sampling Clock Decimation Visible Plot style Color Export Image: Signal I	
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Warning1: the two Analog-to-digital converters in the DAC are hot but can run continuously without problems in a lab environment. A fan may be needed to operate over extended temperature ranges.

Warning2: If the COM-3009 is configured without a DC block, the input signal must be biased positively. In this case, the input signal should never exceed the 0 - 4.5V voltage rail.