

## 3009 -> 8002 -> 5003

- a) Configure the COM-3009 at 40 Msamples/s. DDR output is off. Two jumpers must be inserted, one facing L3, the other facing L10. The jumper J1 should be installed in position 1-2

The screenshot shows the ComBlock Control Center interface. The tree view on the left is expanded to show 'COM3009 Dual Analog to Digital Converter'. A dialog box titled 'COM3009 Dual Analog to Digital Converter Basic S...' is open, displaying the following settings:

- ADC Sampling Frequency: 40000000 Hz
- Internal ADC Sampling Clock
- DDR Output Interface

Buttons at the bottom include 'Apply', 'Ok', 'Advan...', and 'Cancel'.

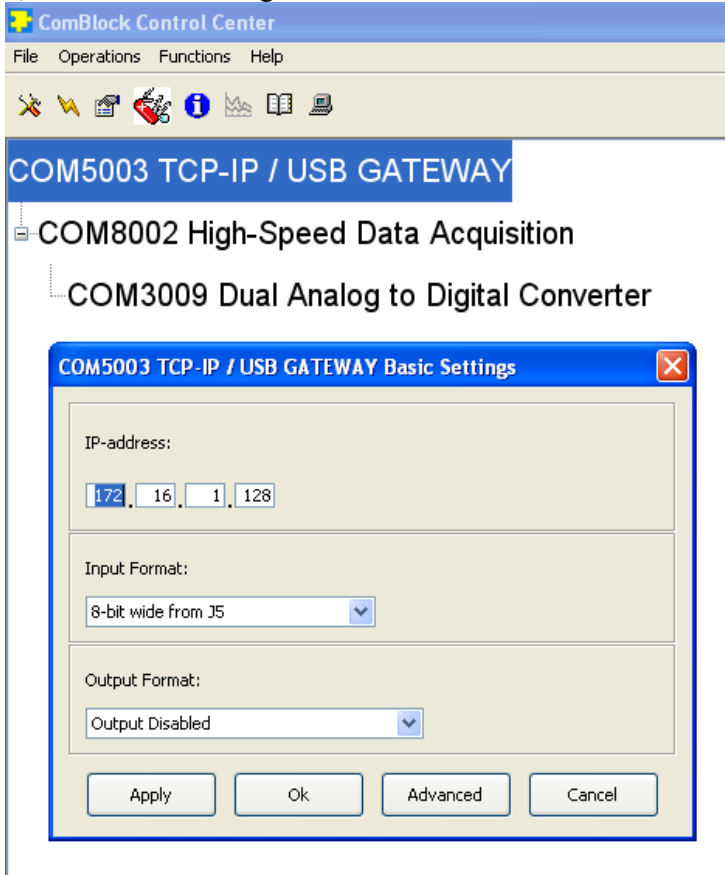
- b) COM-8002 configuration

The screenshot shows the ComBlock Control Center interface. The tree view on the left is expanded to show 'COM8002 High-Speed Data Acquisition'. A dialog box titled 'Logic Analyzer Functions' is open, displaying the following settings:

Start Upload Address	0	Dec
Upload Window Length (Bytes)	1000000	Dec
External trigger	Disabled	
Input Width Option	20	
Decimation	1	
Start Download Address	0	Dec
Download Window Length (Bytes)	1000000	Dec
Download File		...
AGC	Enabled	
AGC gain	129	Dec
Action	Start Upload	

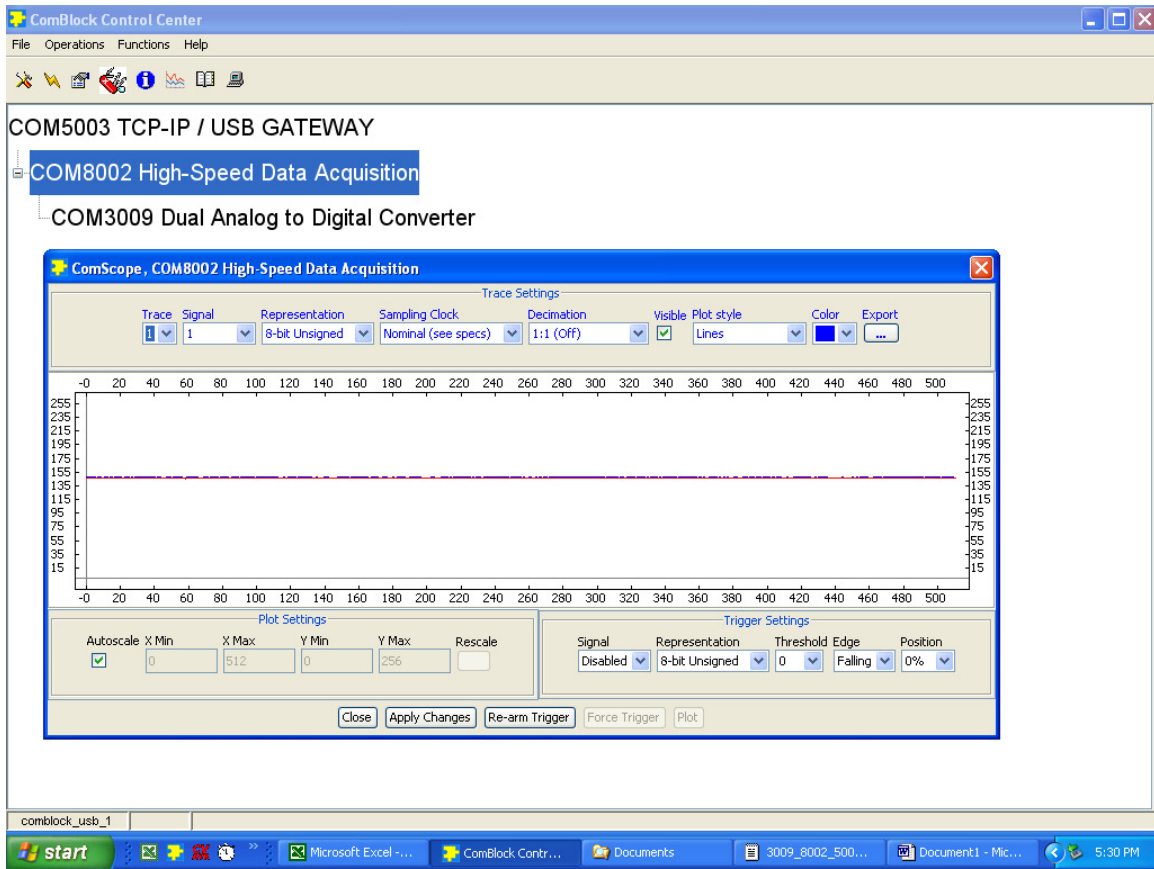
Buttons at the bottom include 'Ok', 'Apply', and 'Cancel'.

### c) COM-5003 configuration



A few check points:

- 1) the overall power consumption of the complete assembly is 1050mA under 5VDC.
- 2) using ComScope, one can visualize the input signals as illustrated below



Warning1: the two Analog-to-digital converters in the DAC are hot but can run continuously without problems in a lab environment. A fan may be needed to operate over extended temperature ranges.

Warning2: If the COM-3009 is configured without a DC block, the input signal must be biased positively. In this case, the input signal should never exceed the 0 – 4.5V voltage rail.